

Schematic Page List

Hide

Global Search

001

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

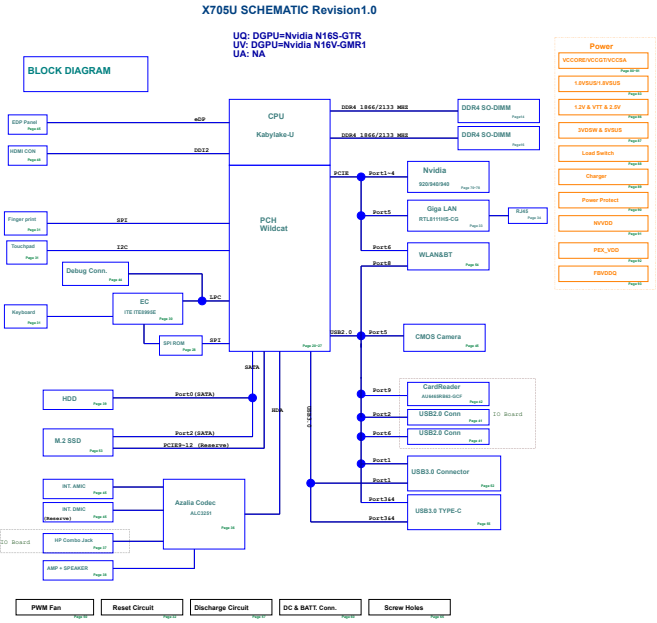
Toggle FullScreen

Pre Page

Next Page

MoveTo

NAME	COMMENT
001	Block Diagram
002	System Setting
003	CPU_DISPLAY
004	CPU_DDR4
005	CPU_LPC,SPI,SMB,CLINK
006	CPU_POEWR
007	CPU_XDP
008	CPU_MISC,JTAG
009	CPU_CFG,RSVD
010	CPU_POWER_CAP
011	
012	
013	DDR4_
014	DDR4 SO-DIMM_A
015	DDR4_
016	DDR4_SO-DIMM_B
017	DDR4_****
018	DDR4_****



Hide

Page

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

Global Search

001

002

003

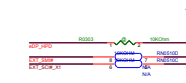
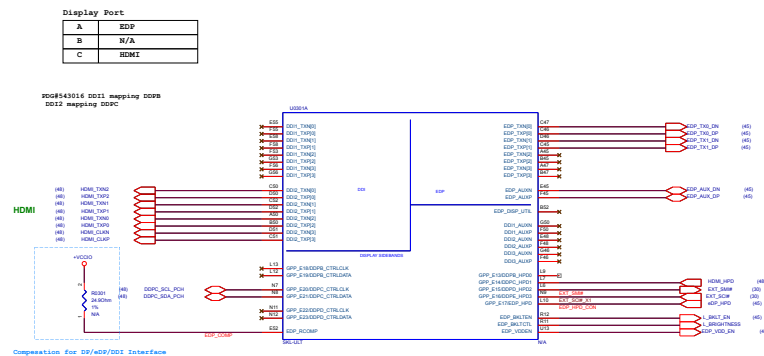
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Schematic Page List

Hide

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4 SO-DIMM_A

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016_DDR4_SO-DIMM_B

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018_DDR4_****

Global Search

001

002

003

004

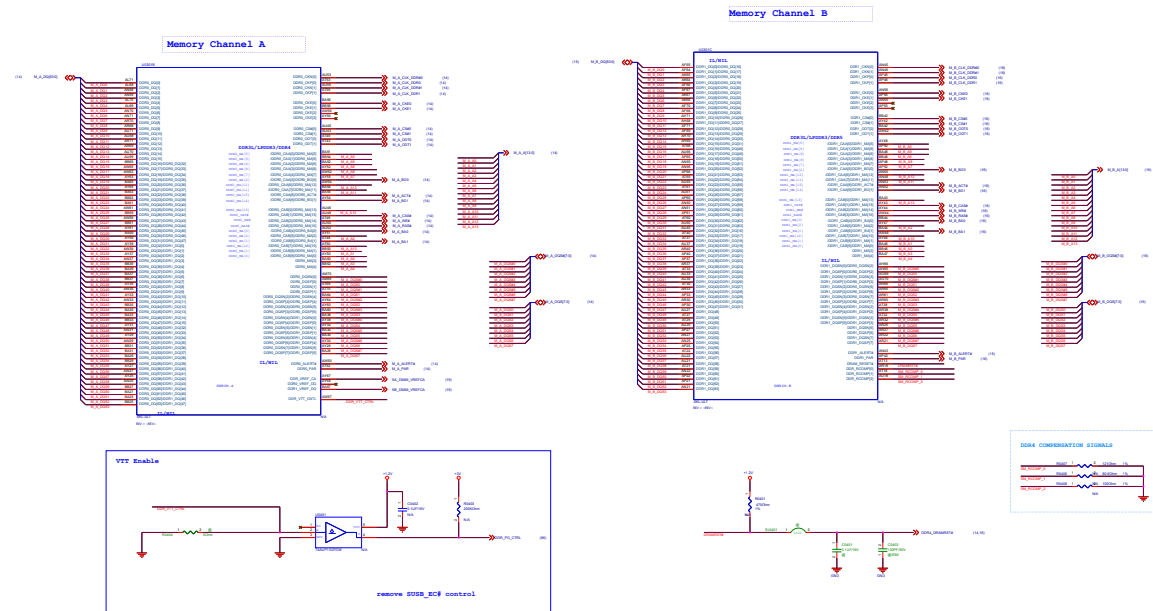
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



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Schematic Page List

Hide

Global Search

001

002

003

004

005

Toggle FullScreen

Pre Page

Next Page

MoveTo

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009 CPU CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

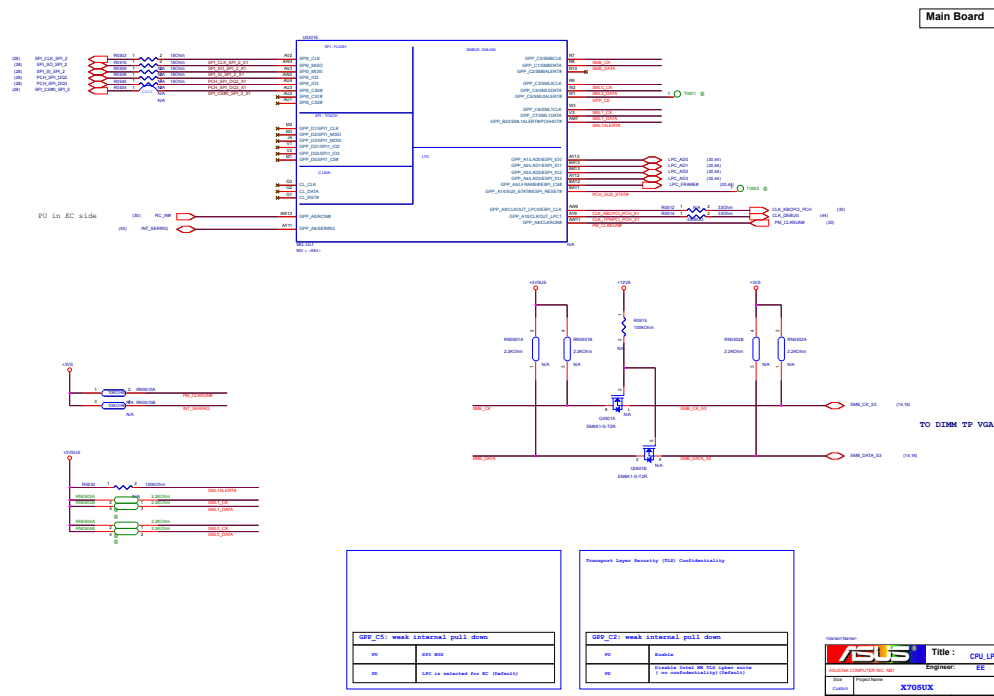
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016_DDR4_SO-DIMM_B

017 DDR4 ****

018_DDR4_****



Schematic Page List

Hide

Global Search

006

007

008

009

010

Toggle FullScreen

Pre Page

Next Page

MoveTo

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

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010_CPU_POWER_CAP

011_

012_

013_DDR4_

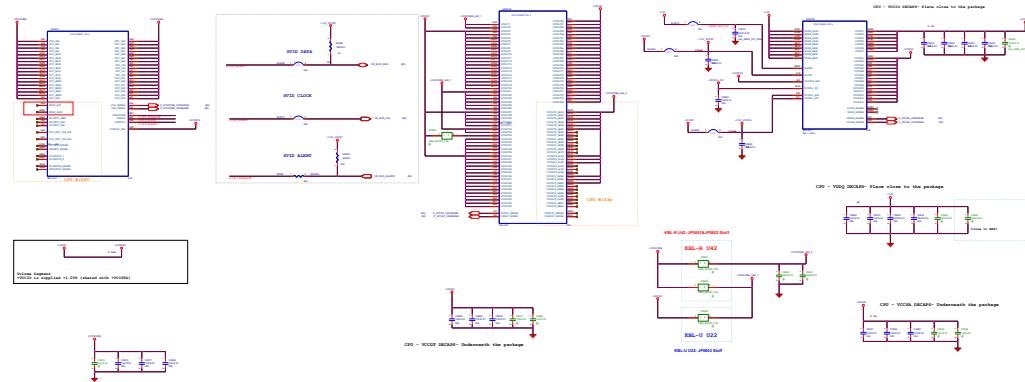
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015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****



Schematic Page List

Hide

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4 ****

018_DDR4 ****

Global Search

006

007

008

009

010

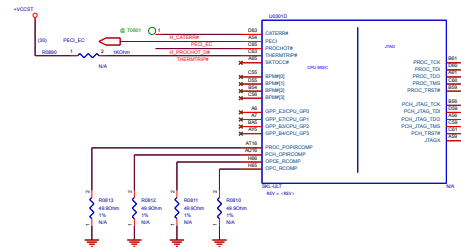
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Pre Page

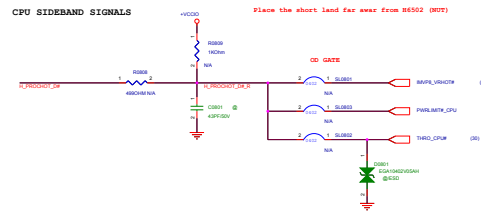
Next Page

MoveTo

Main Board



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ASUS		Title :	CPU_MISC,JTAG,CLK
ASUSTeK COMPUTER INC. (ASUS)		Engineer:	EE
Rev :	1	Project Name :	X708UX
Date :	2017/04/10	Drawn :	EE

Schematic Page List

Hide

Schematic

Page

SCHEMATIC1

- 001_Block Diagram
- 002_System Setting
- 003_CPU_DISPLAY
- 004_CPU_DDR4
- 005_CPU_LPC,SPI,SMB,CLINK
- 006_CPU_POEWR
- 007_CPU_XDP
- 008_CPU_MISC,JTAG
- 009_CPU_CFG,RSVD
- 010_CPU_POWER_CAP
- 011_
- 012_
- 013_DDR4_
- 014_DDR4 SO-DIMM_A
- 015_DDR4_
- 016_DDR4_SO-DIMM_B
- 017_DDR4_****
- 018_DDR4_****

Global Search

006

007

008

009

010

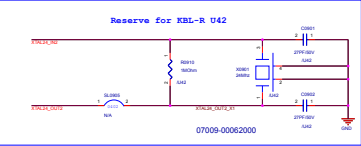
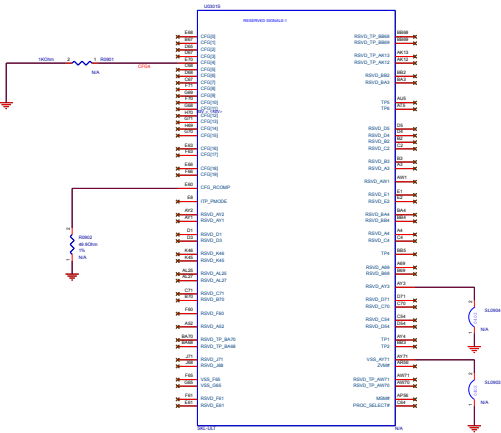
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



	1	0	NOTE
CFG0	NO STALL	STALL	STALL RESET SEQUENCE AFTER STOP SIGNAL DE-ASSERTED
CFG4	DISABLE	ENABLE	#0P ENABLE

ASUS

Title : CPU_CFG_RSVD

Engineer: EE

Rev: 1

Created Name: X766U

Rev: 1

Created Date: 2021-12-15

Rev: 1

Created Date: 2021-12-15

Schematic Page List

Hide

Global Search

006

007

008

009

010

Toggle FullScreen

Pre Page

Next Page

MoveTo

Schematic

Page

SCHEMATIC1

001_Block Diagram

002_System Setting

003_CPU_DISPLAY

004_CPU_DDR4

005_CPU_LPC,SPI,SMB,CLINK

006_CPU_POEWR

007_CPU_XDP

008_CPU_MISC,JTAG

009_CPU_CFG,RSVD

010_CPU_POWER_CAP

011_

012_

013_DDR4_

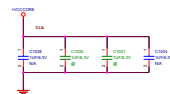
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015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****



CPU - VCC DECAPS- Place close to the package



Schematic Page List

Hide

012_
013_DDR4_
014_DDR4 SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC ITE8995

Global Search

014

016

019

020

021

022

023

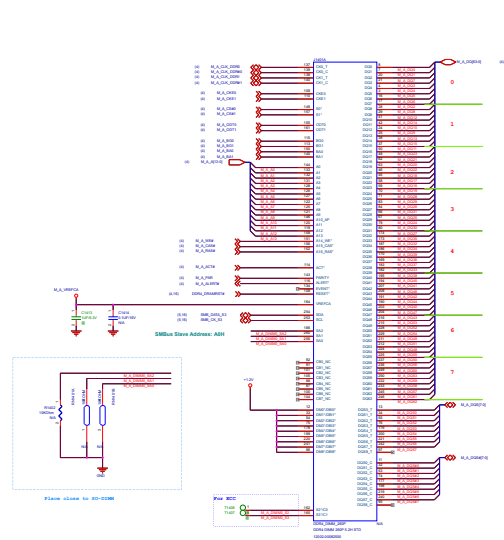
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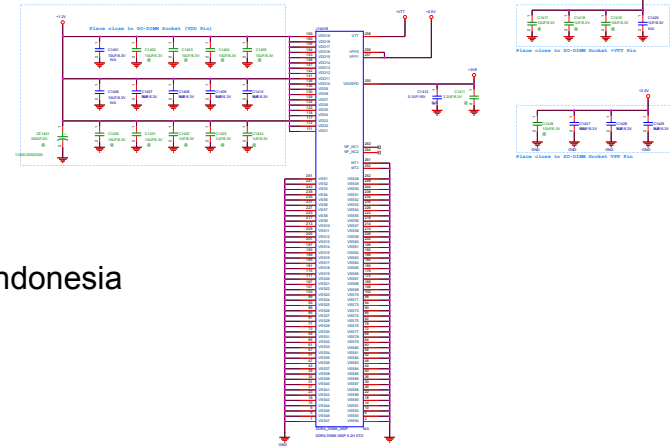
Pre Page

Next Page

MoveTo



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Schematic Page List

Hide

012_
013_DDR4_
014_DDR4 SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
020_CPU_PCH_CSI2,EMMC
021_CPU_PCH_CGPIO, LPIO, MI
SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC ITE8995

Global Search

014

016

019

020

021

022

023

024

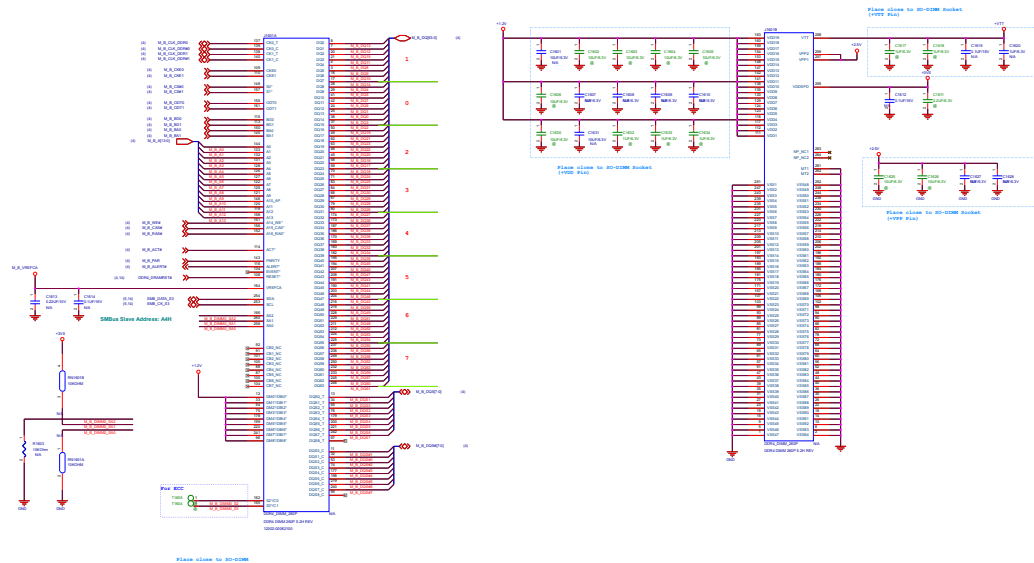
Toggle FullScreen

Pre Page

Next Page

MoveTo

SODIMM CHB-DIMM0



Schematic Page List

Hide

012_
013_DDR4_
014_DDR4 SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
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021_CPU_PCH_CGPIO, LPIO, MI
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022_CPU_PCH_AUDIO,SDIO,SD
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S,RTC
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026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC ITE8995

Global Search

014

016

019

020

021

022

023

024

Toggle FullScreen

Pre Page

Next Page

MoveTo

Close to SO-DIMM B

Close to SO-DIMM A

ASUS

Title : DDR4_CA_DQ_VOLTAGE

Engineer: EE

Checked: XT08VX

Drawn: XT08VX

Rev: 1

File: 016.DD

Schematic Page List	Hide
012_	
013_DDR4_	
014_DDR4 SO-DIMM_A	
015_DDR4_	
016_DDR4_SO-DIMM_B	
017_DDR4_****	
018_DDR4_****	
019_DDR4_CA_DQ_VOLTAGE	
020_CPU_PCH_CSI2,EMMC	
021_CPU_PCH_CGPIO, LPIO, MI SC	
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027_CPU_PCH_POEWR,GND	
028_PCH-SPI ROM,OTH	
029_PCH-XDP	
030_KBC ITE8995	

Global Search

014

016

019

020

021

022

023

024

Toggle FullScreen

Pre Page

Next Page

MoveTo

The diagram shows the Main Board schematic, including various components like CPU, PCH, and memory modules. It includes detailed signal traces and component footprints.

No Redundant

Search: 85255 Storage, 8415 8888

ASUS TUF Gaming X7000K

Schematic Page List

Hide

Global Search

014

016

019

020

021

022

023

024

Toggle FullScreen

Pre Page

Next Page

MoveTo

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

017_DDR4_****

018_DDR4_****

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SC022_CPU_PCH_AUDIO,SDIO,SD
XC

023_CPU_PCH_PCIE,USB,SATA

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

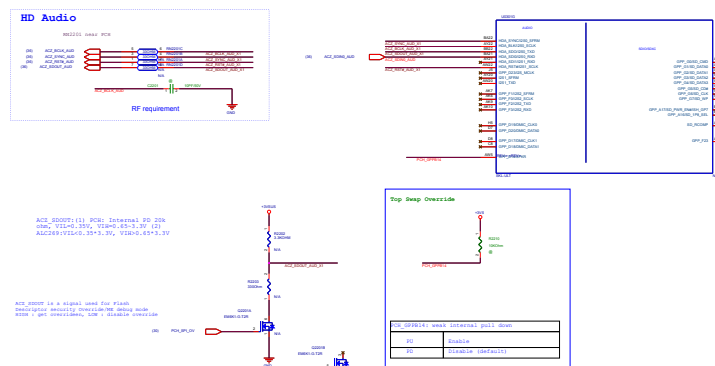
026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030 KBC ITE8995



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Schematic Page List

Hide

012_

013_DDR4_

014_DDR4 SO-DIMM_A

015_DDR4_

016_DDR4_SO-DIMM_B

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025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC ITE8995

Global Search

014

016

019

020

021

022

023

024

Toggle FullScreen

Pre Page

Next Page

MoveTo

The schematic diagram illustrates the USB mapping and connections for the CPU PCH. It shows the internal connections between the CPU PCH and various USB controllers, including USB 2.0, USB 3.0, and USB 3.1. The diagram includes labels for various USB ports and their corresponding pins, as well as a USB Mapping Table.

USB 2.0	USB 3.0
1. External USB 2.0 Port	1. External USB 3.0 Port (Type-C)
2. Type-C USB 3.0 Port	2. Type-C USB 3.0 Port (Type-C)
3. Type-C USB 3.0 Port	3. Type-C USB 3.0 Port (Type-C)
4. External USB 3.0 Port	4. External USB 3.0 Port (Type-C)
5. USB 3.0 Port	5. USB 3.0 Port (Type-C)
6. USB 3.0 Port	6. USB 3.0 Port (Type-C)
7. USB 3.0 Port	7. USB 3.0 Port (Type-C)
8. USB 3.0 Port	8. USB 3.0 Port (Type-C)
9. USB 3.0 Port	9. USB 3.0 Port (Type-C)
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26. USB 3.0 Port	26. USB 3.0 Port (Type-C)
27. USB 3.0 Port	27. USB 3.0 Port (Type-C)
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32. USB 3.0 Port	32. USB 3.0 Port (Type-C)
33. USB 3.0 Port	33. USB 3.0 Port (Type-C)
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42. USB 3.0 Port	42. USB 3.0 Port (Type-C)
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46. USB 3.0 Port	46. USB 3.0 Port (Type-C)
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57. USB 3.0 Port	57. USB 3.0 Port (Type-C)
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59. USB 3.0 Port	59. USB 3.0 Port (Type-C)
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78. USB 3.0 Port	78. USB 3.0 Port (Type-C)
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80. USB 3.0 Port	80. USB 3.0 Port (Type-C)
81. USB 3.0 Port	81. USB 3.0 Port (Type-C)
82. USB 3.0 Port	82. USB 3.0 Port (Type-C)
83. USB 3.0 Port	83. USB 3.0 Port (Type-C)
84. USB 3.0 Port	84. USB 3.0 Port (Type-C)
85. USB 3.0 Port	85. USB 3.0 Port (Type-C)
86. USB 3.0 Port	86. USB 3.0 Port (Type-C)
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88. USB 3.0 Port	88. USB 3.0 Port (Type-C)
89. USB 3.0 Port	89. USB 3.0 Port (Type-C)
90. USB 3.0 Port	90. USB 3.0 Port (Type-C)
91. USB 3.0 Port	91. USB 3.0 Port (Type-C)
92. USB 3.0 Port	92. USB 3.0 Port (Type-C)
93. USB 3.0 Port	93. USB 3.0 Port (Type-C)
94. USB 3.0 Port	94. USB 3.0 Port (Type-C)
95. USB 3.0 Port	95. USB 3.0 Port (Type-C)
96. USB 3.0 Port	96. USB 3.0 Port (Type-C)
97. USB 3.0 Port	97. USB 3.0 Port (Type-C)
98. USB 3.0 Port	98. USB 3.0 Port (Type-C)
99. USB 3.0 Port	99. USB 3.0 Port (Type-C)
100. USB 3.0 Port	100. USB 3.0 Port (Type-C)

ASUS

Title : CPU PCH PCB LAYOUT

Engineer : DE

DATE : 2023-01-01

BY : X7000X

Schematic Page List

Hide

012_
013_DDR4_
014_DDR4 SO-DIMM_A
015_DDR4_
016_DDR4_SO-DIMM_B
017_DDR4_****
018_DDR4_****
019_DDR4_CA_DQ_VOLTAGE
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SC
022_CPU_PCH_AUDIO,SDIO,SD
XC
023_CPU_PCH_PCIE,USB,SATA
024_CPU_PCH_CLOCK SIGNAL
S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
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029_PCH-XDP
030 KBC ITE8995

Global Search

014

016

019

020

021

022

023

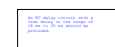
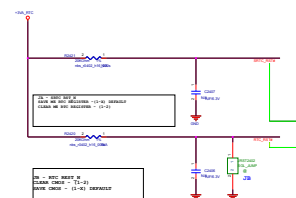
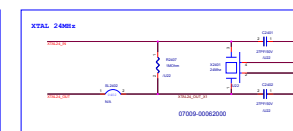
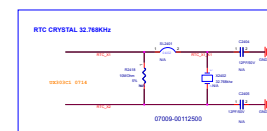
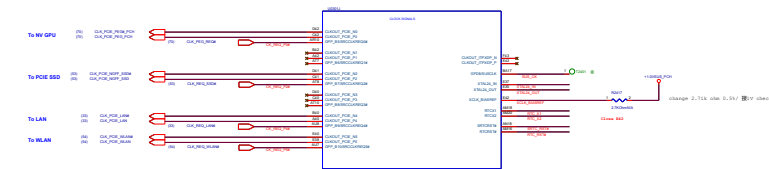
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Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

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S,RTC
025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC_ITE8995
031_KBC_KB & TP & FP
032_RST_Reset Circuit
033_LAN-RTL8111HS-CG
034_LAN_RJ45
035_
036-AUDIO_CODEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
042_IO Board_CR_AU6465R
043_

Global Search

025

026

027

028

029

030

031

032

033

034

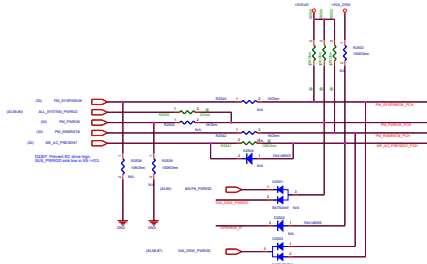
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Power failure solution (B0~C03.00~C03.01)

Schematic Page List

Hide

S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
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043_

Global Search

025

026

027

028

029

030

031

032

033

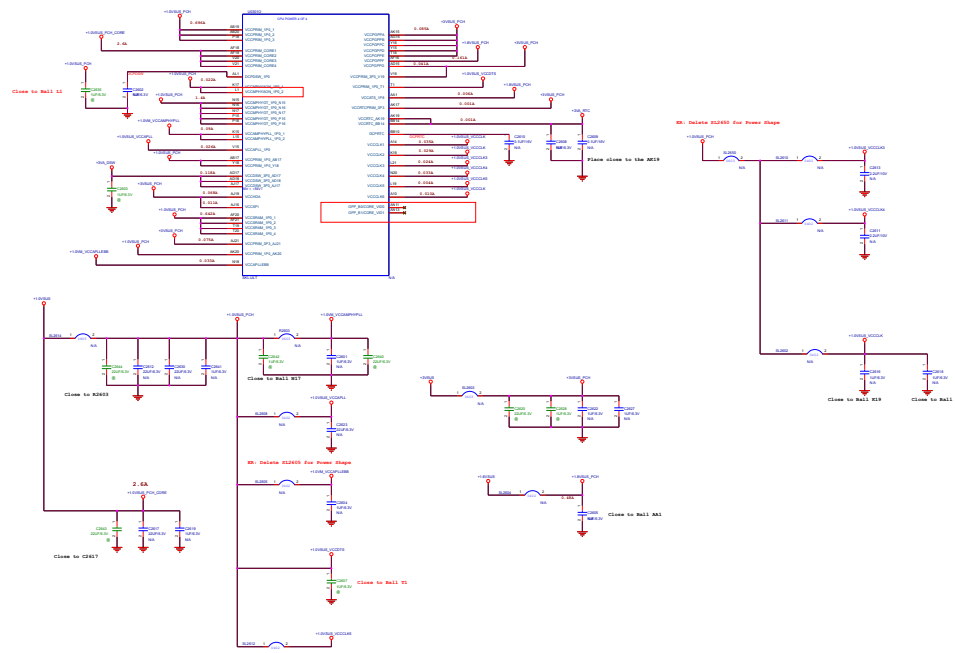
034

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

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025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
027_CPU_PCH_POEWR,GND
028_PCH-SPI ROM,OTH
029_PCH-XDP
030_KBC_ITE8995
031_KBC_KB & TP & FP
032_RST_Reset Circuit
033_LAN-RTL8111HS-CG
034_LAN_RJ45
035_
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038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
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043_

Global Search

025

026

027

028

029

030

031

032

033

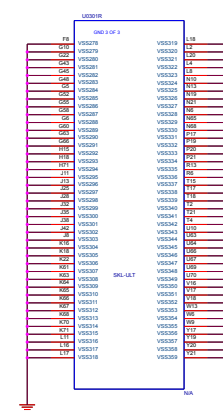
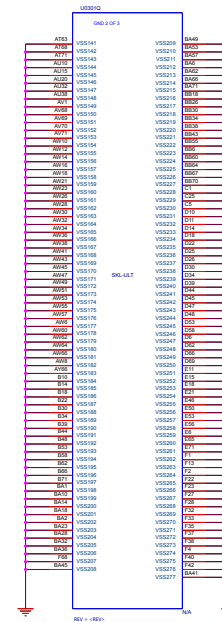
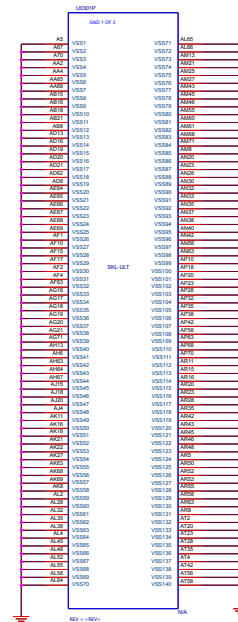
034

Toggle FullScreen

Pre Page

Next Page

MoveTo



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ASUS		Title : CPU_PCH_POEWR,GND	
ALUSTAR COMPUTER INC. BCS		Engineer: EE	
Size	Project Name	Rev	Rev
B	UX303	1	1
Date : 04/04/2017		Sheet	27 of 30

Schematic Page List

Hide

024_CPU_FCH_ELEC_SIGNAL

S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

027_CPU_PCH_POEWR,GND

028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
rew

042_IO Board_CR_AU6465R

043_

Global Search

025

026

027

028

029

030

031

032

033

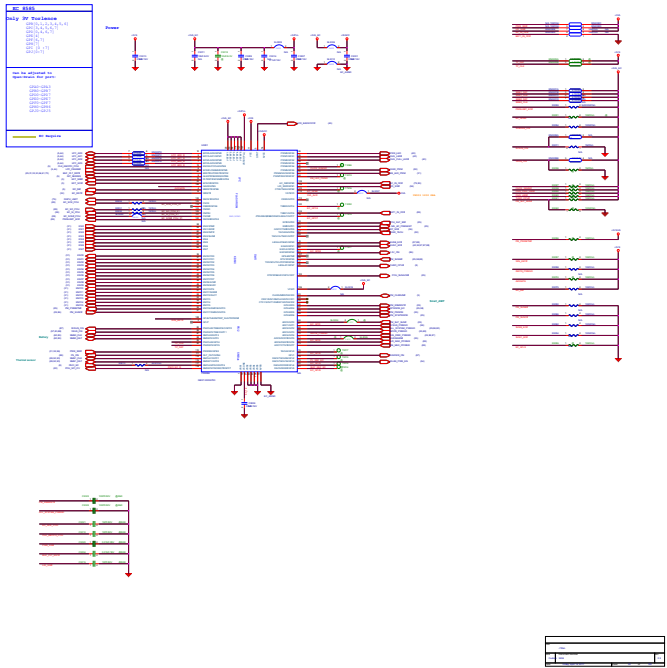
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Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

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S,RTC
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029_PCH-XDP
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031_KBC_KB & TP & FP
032_RST_Reset Circuit
033_LAN-RTL8111HS-CG
034_LAN_RJ45
035_
036-AUDIO_CODEC
037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
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043_

Global Search

025

026

027

028

029

030

031

032

033

034

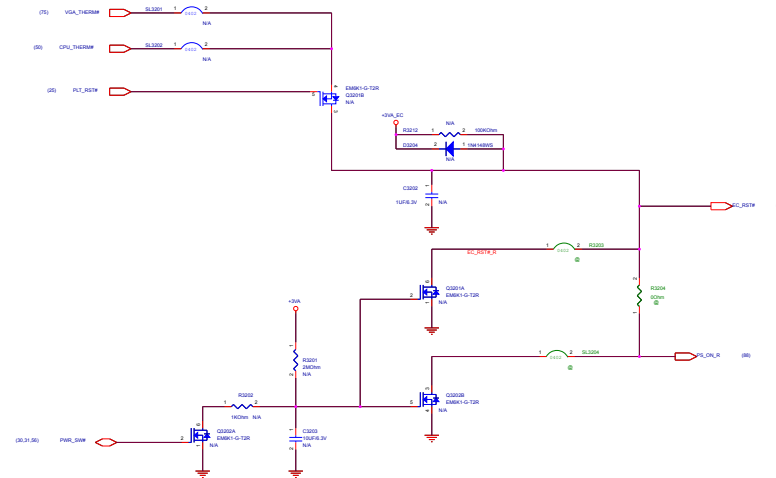
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



ASUS		Title : RST_Reset Circuit
Project Name : RST_Reset		Engineer : SZHBT
Rev : 0	Project Name : X705UX	Rev : 0
Date : 2015/04/15	Drawn : SZHBT	Check : SZHBT

Schematic Page List

Hide

025_CPU_PCH_SYS_POWER
026_CPU_PCH_POEWR,GND
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034_LAN_RJ45
035_
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037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
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043_

Global Search

025

026

027

028

029

030

031

032

033

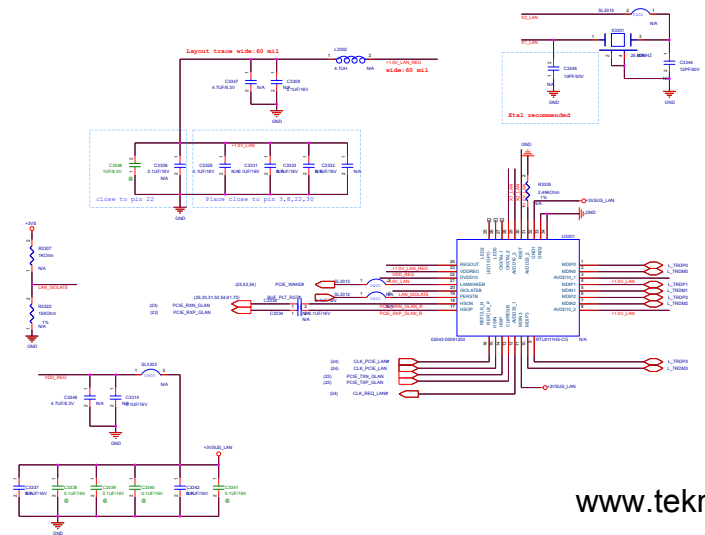
034

Toggle FullScreen

Pre Page

Next Page

MoveTo

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Schematic Page List

Hide

024_CPU_PCH_CLOCK SIGNAL
S,RTC

025_CPU_PCH_SYS_POWER

026_CPU_PCH_POEWR,GND

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028_PCH-SPI ROM,OTH

029_PCH-XDP

030_KBC_ITE8995

031_KBC_KB & TP & FP

032_RST_Reset Circuit

033_LAN-RTL8111HS-CG

034_LAN_RJ45

035_

036-AUDIO_CODEC

037_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
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043_

Global Search

025

026

027

028

029

030

031

032

033

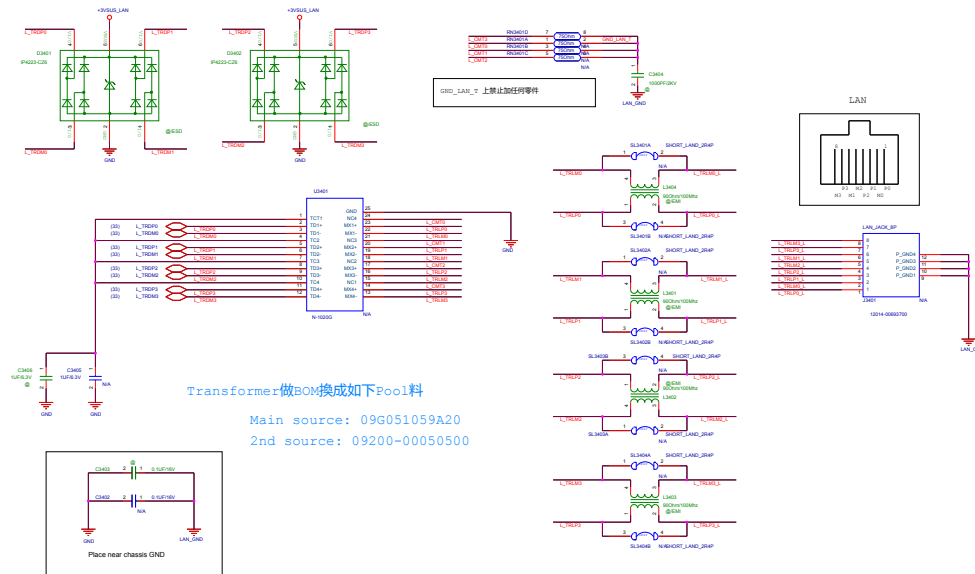
034

Toggle FullScreen

Pre Page

Next Page

MoveTo



Transformer做BOM换成如下Pool料

Main source: 09G051059A20

2nd source: 09200-00050500



Schematic Page List

Hide

- 024_CPU_PCH_CLOCK SIGNAL S,RTC
- 025_CPU_PCH_SYS_POWER
- 026_CPU_PCH_POEWR,GND
- 027_CPU_PCH_POEWR,GND
- 028_PCH-SPI ROM,OTH
- 029_PCH-XDP
- 030_KBC_ITE8995
- 031_KBC_KB & TP & FP
- 032_RST_Reset Circuit
- 033_LAN-RTL8111HS-CG
- 034_LAN_RJ45
- 035_
- 036-AUDIO_CODEC
- 037_
- 038_AUD-SPEAKER
- 039_SATA_HDD_CONN
- 040_B to B Connector
- 041_IO Board_USB_LED_FPC_Screw
- 042_IO Board_CR_AU6465R
- 043_

Global Search

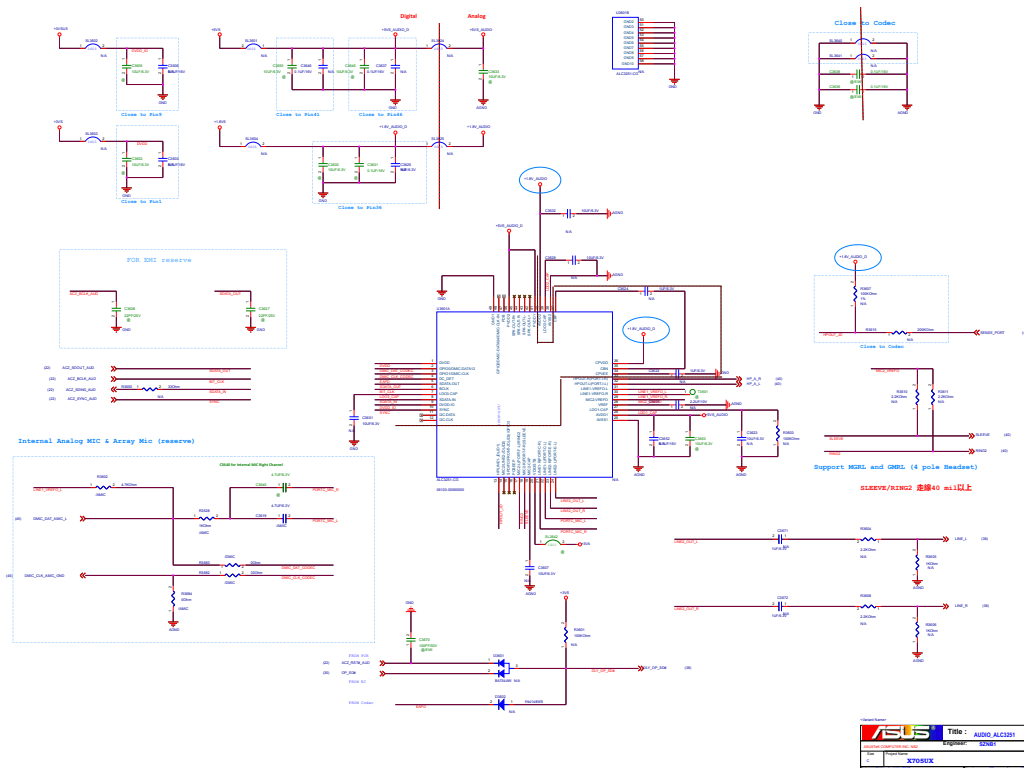
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Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

037_
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039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
042_IO Board_CR_AU6465R
043_
044_DEBUG_LPC
045_LCD_eDP_CMOS_DMIC
046_
047_
048_HDMI-Type-D
049_****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge

Global Search

038

039

040

041

042

044

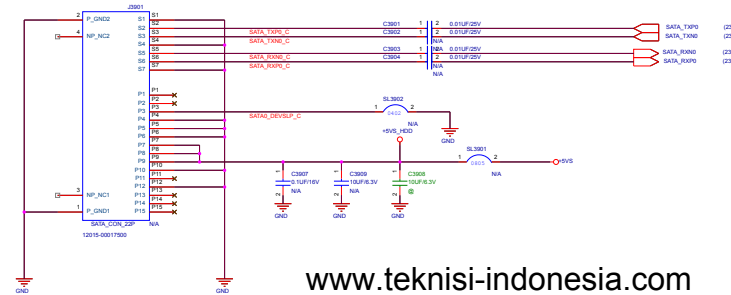
045

Toggle FullScreen

Pre Page

Next Page

MoveTo



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ASUSTeK COMPUTER INC. H60		Engineer: SZ-NB	
Date	Project Name	Rev	
C	X756U	01.0	
Date: Friday, April 16, 2015		Sheet	38 of 38

Schematic Page List

Hide

Global Search

038

039

040

041

042

044

045

Toggle FullScreen

Pre Page

Next Page

MoveTo [

057_

038_AUD-SPEAKER

039_SATA_HDD_CONN

040_B to B Connector

041_IO Board_USB_LED_FPC_Sc
rew

042_IO Board_CR_AU6465R

043_

044_DEBUG_LPC

045_LCD_eDP_CMOS_DMIC

046_

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

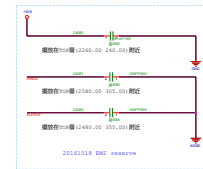
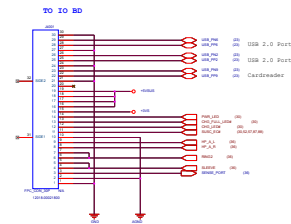
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054_NGFF_WLAN_BT

055_USB3.0_TYPEC

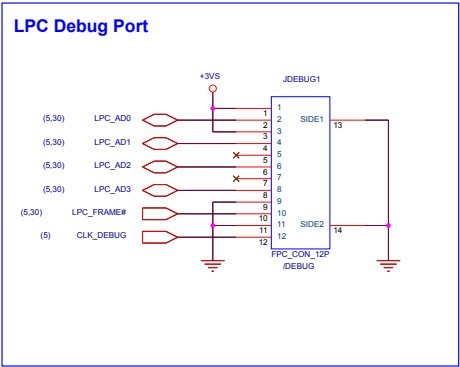
056_Hall Sensor & Power Switch

057 DSG Discharge



Schematic Page List	Hide
037_	
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041_IO Board_USB_LED_FPC_Screw	
042_IO Board_CR_AU6465R	
043_	
044_DEBUG_LPC	
045_LCD_eDP_CMOS_DMIC	
046_	
047_	
048_HDMI-Type-D	
049_*****	
050_Thermal Sensor & Fan	
051_	
052_USB3.0 PORT	
053_NGFF_SSD_CONN	
054_NGFF_WLAN_BT	
055_USB3.0_TYPEC	
056_Hall Sensor & Power Switch	
057_DSG_Discharge	

Global Search	038	039	040	041	042	044	045		
Toggle FullScreen							Pre Page	Next Page	MoveTo <input type="text"/>



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ASUSTek COMPUTER INC.		Engineer: SZNB2	
Size	Project Name	Rev	
A	X705UX	R1.0	
Date: Friday, April 14, 2017	Sheet 44 of 102		

Schematic Page List

Hide

037_
038_AUD-SPEAKER
039_SATA_HDD_CONN
040_B to B Connector
041_IO Board_USB_LED_FPC_Sc
rew
042_IO Board_CR_AU6465R
043_
044_DEBUG_LPC
045_LCD_eDP_CMOS_DMIC
046_
047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
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056_Hall Sensor & Power Switch
057_DSG_Discharge

Global Search

038

039

040

041

042

044

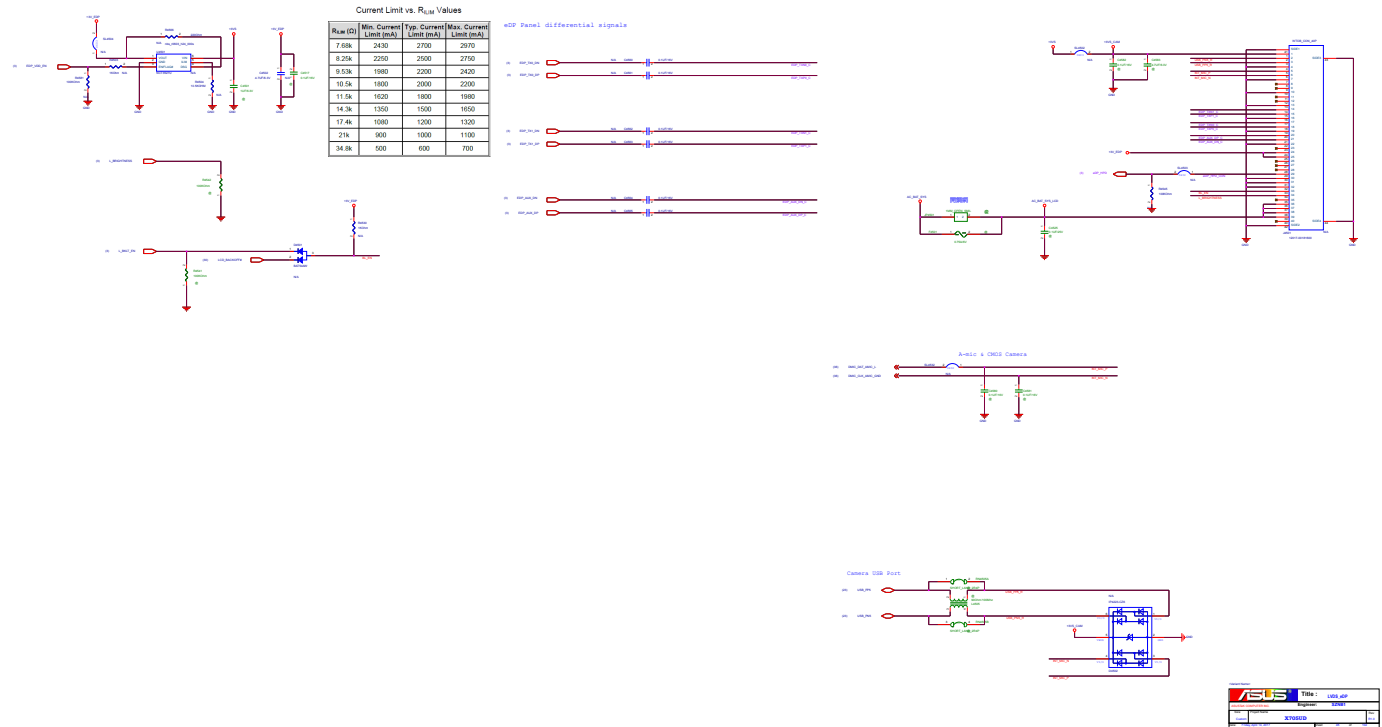
045

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_*****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_*****
063_*****
064_*****
065_SKEW_HOLE_SMT_NUT
066_*****
067_*****

Global Search

048

050

052

053

054

055

056

057

058

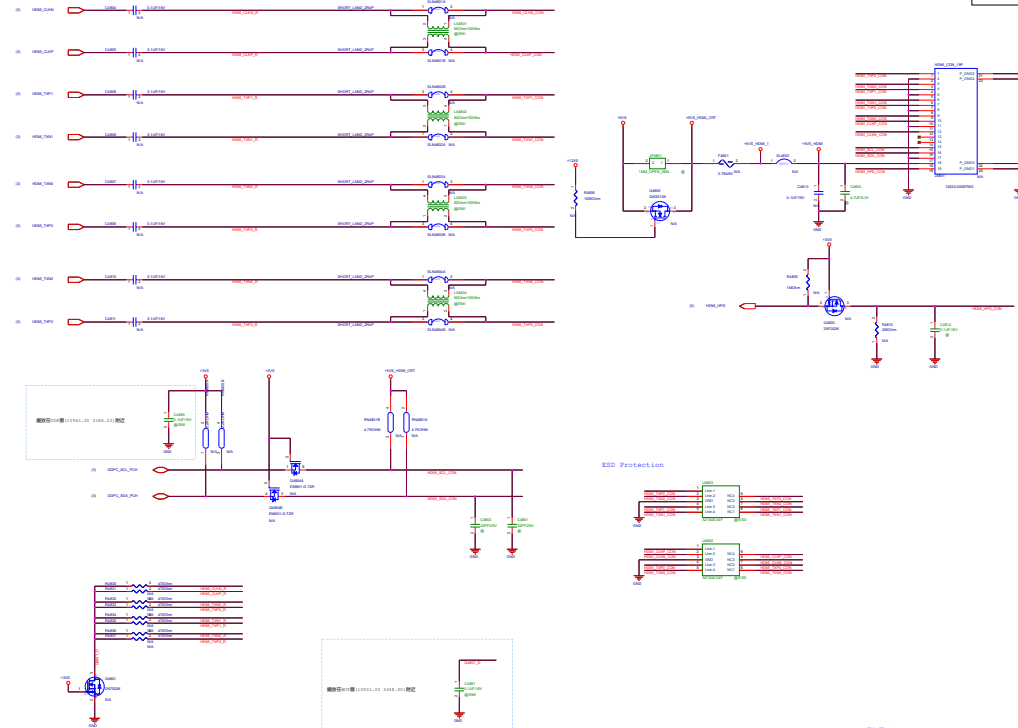
Toggle FullScreen

Pre Page

Next Page

MoveTo

Main Board



Schematic Page List

Hide

Global Search

048

050

052

053

054

055

056

057

058

Toggle FullScreen

Pre Page

Next Page

MoveTo [

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

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057_DSG_Discharge

058_PRO_Protect

059_*****

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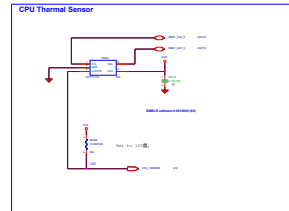
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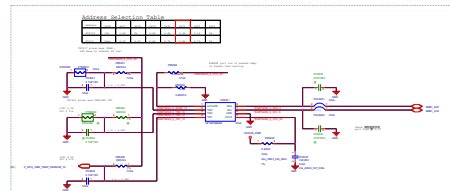
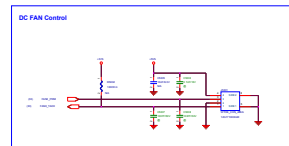
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066_*****

067_*****



Main Board



Schematic Page List

Hide

047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_*****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_*****
063_*****
064_*****
065_SKEW_HOLE_SMT_NUT
066_*****
067_*****

Global Search

048

050

052

053

054

055

056

057

058

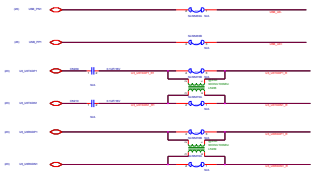
Toggle FullScreen

Pre Page

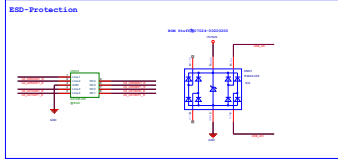
Next Page


MoveTo


USB3.0_Port 0




ESD-Protection









- 047_
- 048_HDMI-Type-D
- 049_*****
- 050_Thermal Sensor & Fan
- 051_
- 052_USB3.0 PORT
- 053_NGFF_SSD_CONN
- 054_NGFF_WLAN_BT
- 055_USB3.0_TYPEC
- 056_Hall Sensor & Power Switch
- 057_DSG_Discharge
- 058_PRO_Protect
- 059_*****
- 060_PW_DC_DC & BAT IN
- 061_EMI_RESERVE
- 062_*****
- 063_*****
- 064_*****
- 065_SKEW_HOLE_SMT_NUT
- 066_*****
- 067_*****

36.3.2.4 AC Capacitor General Guidelines for M.2 SSD Storage Routing on SATA / PCIe Express* Multiplexed Ports

The following table summarizes the AC capacitor requirements on the motherboard when using the SATA/PCIe* multiplexed ports.

Note: When SATA and PCIe* are muxed, always route according to SATA design guidelines. SATA does not support signal polarity reversal and does not support lane reversal.

Table 36-7. SATA / PCIe Express* Gen 2 and Gen 3 Capacitor Values

Conditions	PCI Express* Gen 2 Only	PCI Express* Gen 3 Only	SATA Only	PCI Express* Gen 2 / SATA	PCI Express* Gen 3 / SATA
Processor Tx	100 nF	250 nF	10 nF	100 nF	250 nF
Processor Rx	None	None	10 nF ²	None	None ³

Notes:

- Design Constraint: For PCIe only application, please refer to the PCIe guidelines for details.
- Design Constraint: For SATA only application, both Tx and Rx channels need to have 10 nF capacitors on the motherboard. This option supports all SATA speeds. However, the 10 nF capacitor on Rx can be removed if DC coupled ODDs / devices are not used.
- Design Constraint: For PCIe* Gen 2 / SATA multiplexed configuration, motherboard Tx requires a 100 nF AC capacitor and no AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**
- Design Constraint: For PCIe* Gen 3 / SATA multiplexed configuration, motherboard Tx requires a 250 nF AC capacitor and no AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**
- Design Constraints: Required: Refer to chapter 3, "General Differential Signals Design Guidelines" along with the additional guidelines in this section for all design optimization guidelines.
- Design Constraint: For PCIe* and that needs to support either PCIe* Gen2 Devices or PCIe* Gen3 devices, follow the PCIe* Gen 3 / SATA multiplexed configuration where the motherboard Tx requires a 250 nF AC capacitor and no AC capacitor is required for motherboard Rx channel. **This option DOES NOT support DC coupled ODDs / Devices.**

ASUS Title : NGFF_SSD_03.0
Engineer : BT
XTREME

Schematic Page List

Hide

047_
048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_*****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_*****
063_*****
064_*****
065_SKEW_HOLE_SMT_NUT
066_*****
067_*****

Global Search

048

050

052

053

054

055

056

057

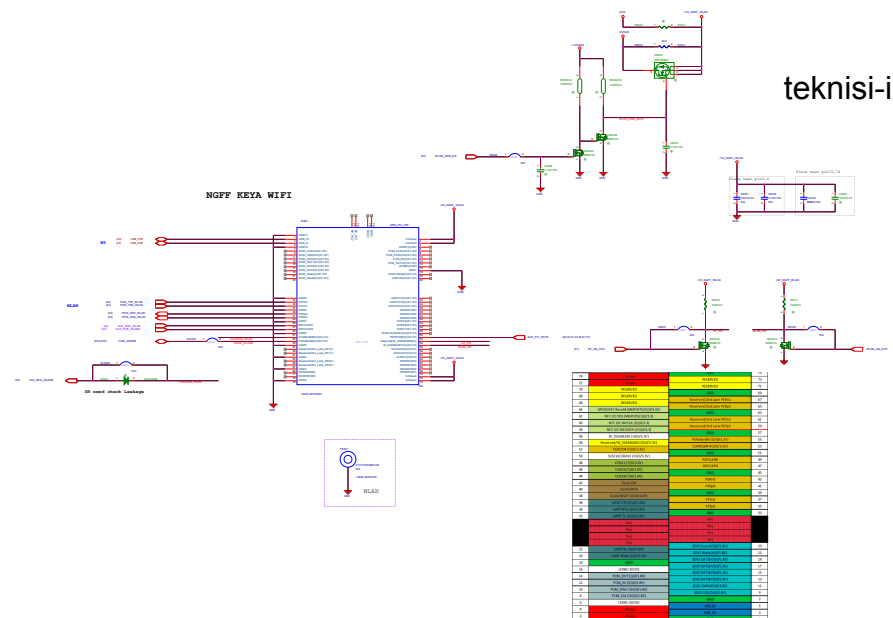
058

Toggle FullScreen

Pre Page

Next Page

MoveTo



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Schematic Page List

Hide

047_
048_HDMI-Type-D
049_****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_****
063_****
064_****
065_SKEW_HOLE_SMT_NUT
066_****
067_****

Global Search

048

050

052

053

054

055

056

057

058


Toggle FullScreen

Pre Page

Next Page

MoveTo

CAP LED



ASUS

Title: ASUS Laptop H3100-01

Rev: 1.0

20180801

1/1

Schematic Page List

Hide

047_

048_HDMI-Type-D

049_****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053_NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

056_Hall Sensor & Power Switch

057_DSG_Discharge

058_PRO_Protect

059_****

060_PW_DC_DC & BAT IN

061_EMI_RESERVE

062_****

063_****

064_****

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066_****

067_****

Global Search

048

050

052

053

054

055

056

057

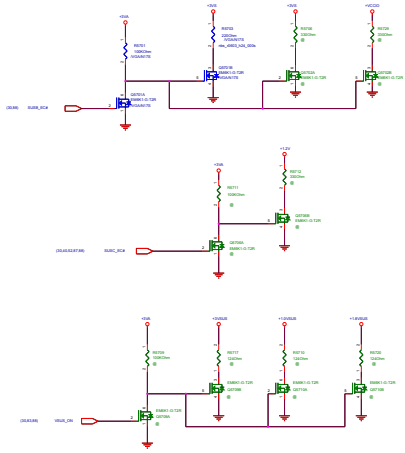
058

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

Global Search

048

050

052

053

054

055

056

057

058

Toggle FullScreen

Pre Page

Next Page

MoveTo

047_

048_HDMI-Type-D

049 *****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF SSD CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

056_Hall Sensor & Power Switch

057_DSG_Discharge

058_PRO_Protect

059 *****

060_PW_DC_DC & BAT IN

061_EMI_RESERVE

062 *****

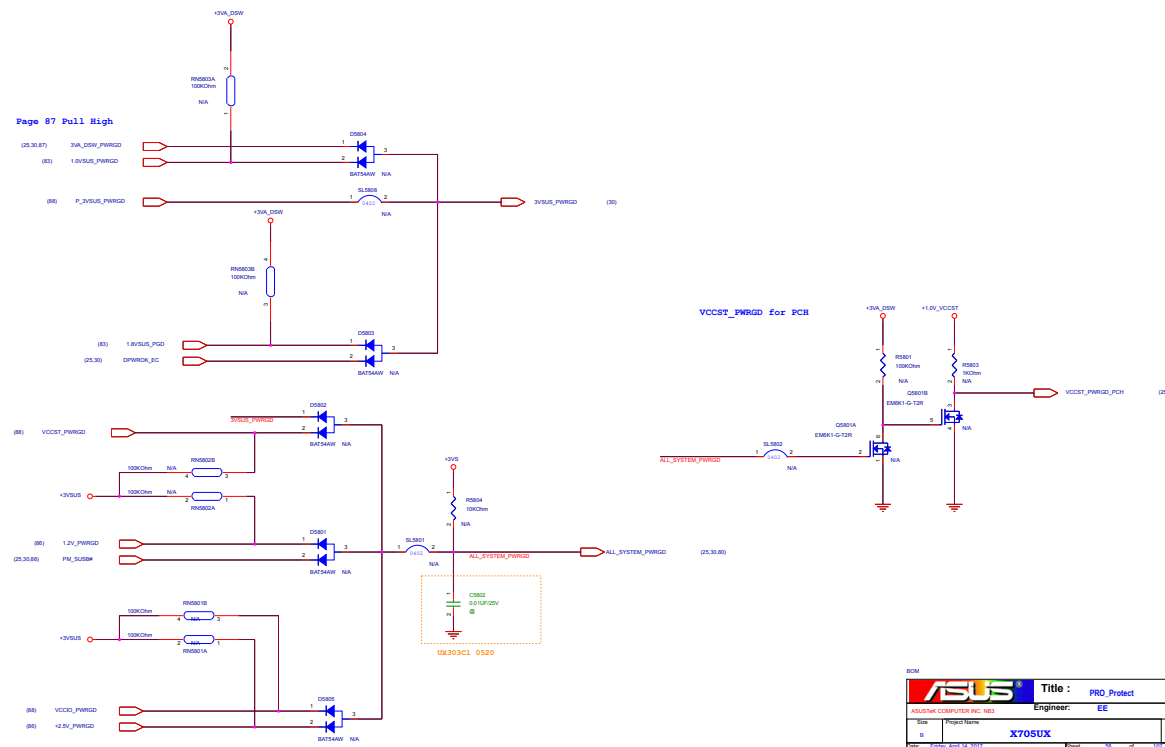
063 *****

064 *****

065_SKEW_HOLE_SMT_NUT

066 *****

067 *****



Schematic Page List

Hide

047_
048_HDMI-Type-D
049_****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_****
063_****
064_****
065_SKEW_HOLE_SMT_NUT
066_****
067_****

Global Search

060

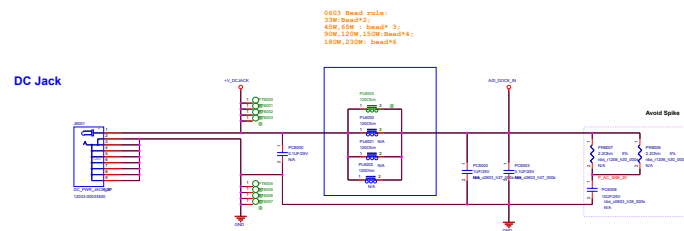
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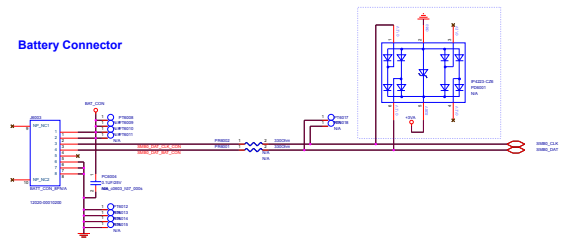
Next Page

MoveTo

DC Jack

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Battery Connector



Schematic Page List

Hide

Global Search

060

061

Toggle FullScreen

Pre Page

Next Page

MoveTo [

047_

048_HDMI-Type-D

049_*****

050_Thermal Sensor & Fan

051_

052_USB3.0 PORT

053. NGFF_SSD_CONN

054_NGFF_WLAN_BT

055_USB3.0_TYPEC

056_Hall Sensor & Power Switch

057_DSG_Discharge

058_PRO_Protect

059_*****

060_PW_DC_DC & BAT IN

061_EMI_RESERVE

062_*****

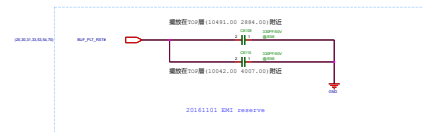
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064_*****

065_SKEW_HOLE_SMT_NUT

066_*****

067_*****



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Hide

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048_HDMI-Type-D
049_*****
050_Thermal Sensor & Fan
051_
052_USB3.0 PORT
053_NGFF_SSD_CONN
054_NGFF_WLAN_BT
055_USB3.0_TYPEC
056_Hall Sensor & Power Switch
057_DSG_Discharge
058_PRO_Protect
059_*****
060_PW_DC_DC & BAT IN
061_EMI_RESERVE
062_*****
063_*****
064_*****
065_SKEW_HOLE_SMT_NUT
066_*****
067_*****

Global Search

060

061

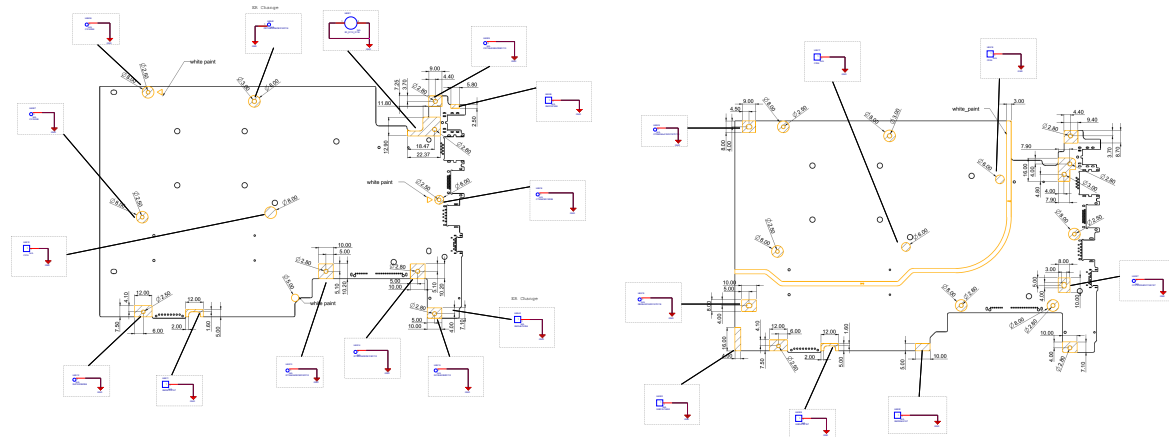
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Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

069_****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_x16 (Bit0~31)

073_VGA_GDDR5_x16 (Bit32~63)

074_VGA_nVIDIA_N16V/S_DISPL
AY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)087_PW_1.0V/+VTT/+2.5V(UP900
2)

Global Search

070

071

072

073

074

075

Toggle FullScreen

Pre Page

Next Page

MoveTo

PCI EXPRESS

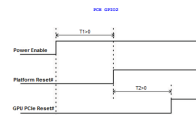
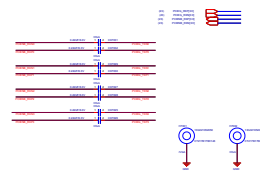


Figure 18-4. Cold Reset Sequence Requirement for Optimus

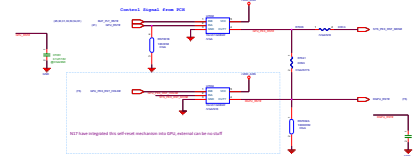


Table 6. PEX Core and IO Supply Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population	N16	N17	Location
N16 PEX_IOVDD (N17 PEX_DVDD) Supply Rail						
GB2B-64, GB2C-64	1.0 μ F	X65	0402	1	1	Under GPU
	4.7 μ F	X65	0603	0	1	Under GPU
	4.7 μ F	X65	0603	1	2	Near GPU
	10 μ F	X65	0805	0	2	Midway between GPU and Power Supply
	22 μ F	X65	0805	0	1	Midway between GPU and Power Supply
N16 PEX_IOVDDQ (N17 PEX_HVDD) Supply Rail						
GB2B-64, GB2C-64	1.0 μ F	X65	0402	1	1	Under GPU
	4.7 μ F	X65	0603	1	1	Near GPU
	10 μ F	X65	0805LP	1	2	Midway between GPU and Power Supply
	22 μ F	X65	0805LP	1	1	Midway between GPU and Power Supply

Note: All GPIO, I2C/A/B/C/S, DAC, H/V, SYNC, PCIe Reset, CLKREQ, XTAL, and JTAG signals, when pulled-up, should be connected to 3V3_AGN power rail. Also PEX_PLL_HVDD and PEX_3V3_SVDD must be sourced by 3V3_AGN.

Table 7. PEX PLLs Decoupling and Filtering

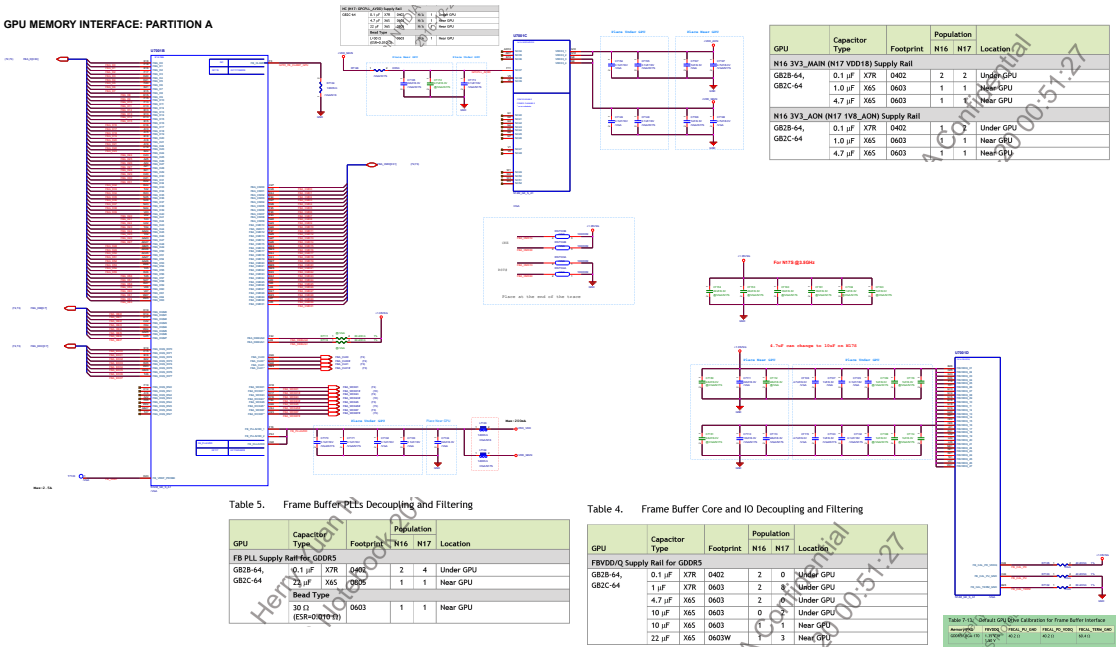
GPU	Capacitor Type	Footprint	Population	N16	N17	Location
PEX_PLLVDD Supply Rail						
GB2B-64	0.1 μ F	X7R	0402	1	N/A	Under GPU
	1.0 μ F	X5R	0603	1	N/A	Near GPU
	4.7 μ F	X5R	0805	1	N/A	Near GPU
PEX_SVDD_3V3 Supply Rail						
GB2B-64	4.7 μ F	X5R	0603	2	N/A	Near GPU
PEX_PLL_HVDD Supply Rail						
GB2B-64, GB2C-64	0.1 μ F	X7R	0402	1	1	Near GPU



Schematic Page List

Hide

- 069_*****
- 070_GPU_PCI-EXPRESS
- 071_GPU_MEMORY INTERFACE
- 072_VGA_GDDR5_x16 (Bit0~31)
- 073_VGA_GDDR5_x16 (Bit32~63)
- 074_VGA_nVIDIA_N16V/S_DISPL
AY
- 075_GPU_GPIO_THERM
- 076_GPU_XTAL/STRAPPING
- 077_GPU_PWG/GND
- 078_GPU_Power_Sel
- 079_*****
- 080_PW_KABY LAKE-U(1)
- 081_PW_KABY LAKE-U(2)
- 082_PW_***
- 083_PW_+1.0VSUS/1.8VSUS(UP
9002)
- 084_PW_
- 085_PW_***
- 086_PW_1.2V/+VTT/+2.5V(UP900
2)
- 087_PW_+0.9VSUS/+1.0VSUS(UP900
2)



Schematic Page List

Hide

069_****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_x16 (Bit0~31)

073_VGA_GDDR5_x16 (Bit32~63)

074_VGA_nVIDIA_N16V/S_DISPLAY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_1.0V/+VTT/+2.5V(UP9002)

Global Search

070

071

072

073

074

075

Toggle FullScreen

Pre Page

Next Page

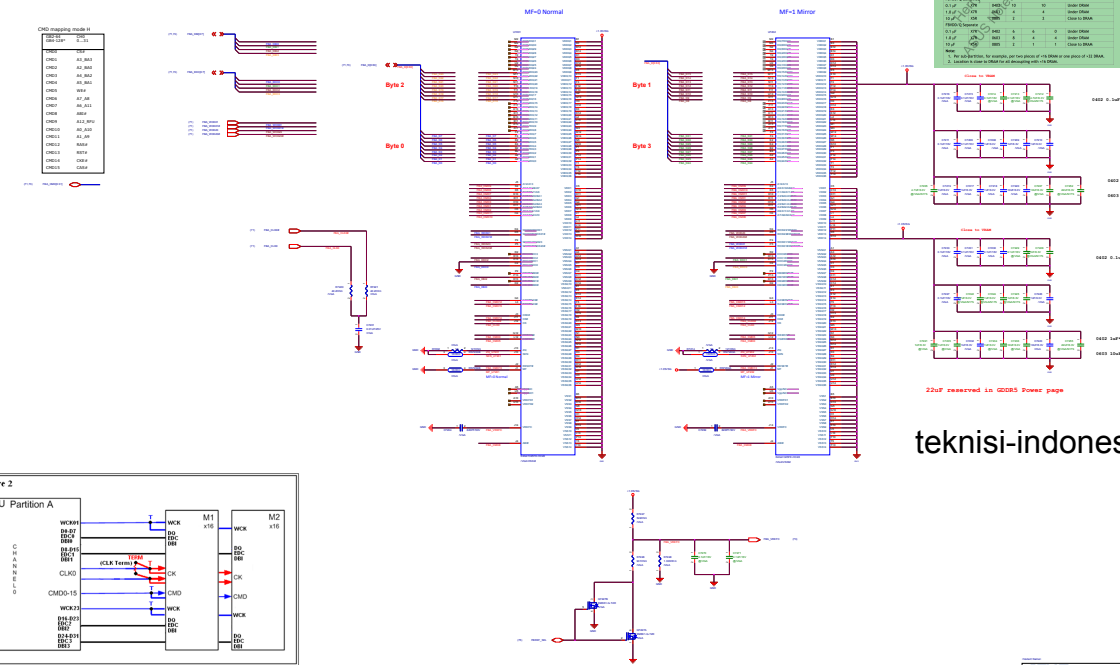
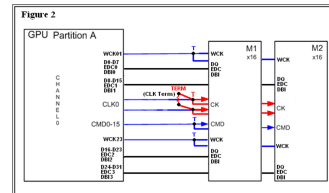
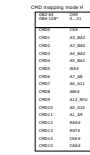
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GDDR5 X16 (Bit0~31)

For 16 GB VRAM Use GDDR5 X16 Mode
For 32 GB VRAM Use GDDR5 X16 Mode

CMIO Mapping Table

CMIO	Device
CMIO0	0000_0000
CMIO1	0000_0000
CMIO2	0000_0000
CMIO3	0000_0000
CMIO4	0000_0000
CMIO5	0000_0000
CMIO6	0000_0000
CMIO7	0000_0000
CMIO8	0000_0000
CMIO9	0000_0000
CMIO10	0000_0000
CMIO11	0000_0000
CMIO12	0000_0000
CMIO13	0000_0000
CMIO14	0000_0000
CMIO15	0000_0000



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Schematic Page List

Hide

069_****

070_GPU_PCI-EXPRESS

071_GPU_MEMORY INTERFACE

072_VGA_GDDR5_x16 (Bit0~31)

073_VGA_GDDR5_x16 (Bit32~63)

074_VGA_nVIDIA_N16V/S_DISPLAY

075_GPU_GPIO_THERM

076_GPU_XTAL/STRAPPING

077_GPU_PWG/GND

078_GPU_Power_Sel

079_****

080_PW_KABY LAKE-U(1)

081_PW_KABY LAKE-U(2)

082_PW_***

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+0.9VSUS/+1.0VSUS(UP9002)

Global Search

070

071

072

073

074

075

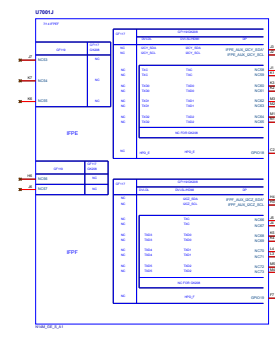
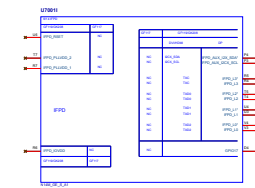
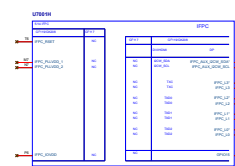
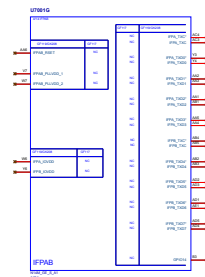
Toggle FullScreen

Pre Page

Next Page

MoveTo

LVDS



CRT



Hide

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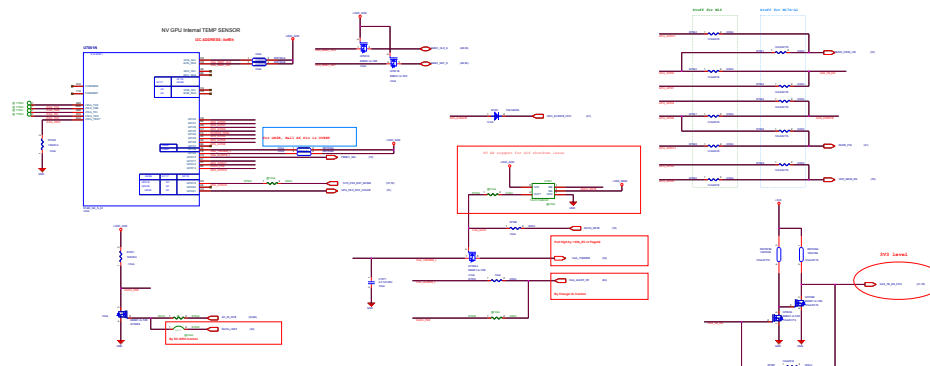
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Pre Page

Next Page

MoveTo

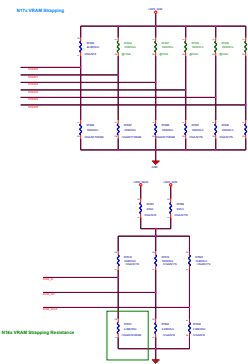
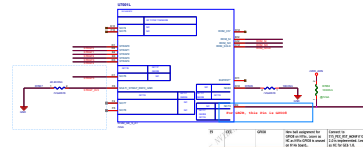
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GPIN1	GPIN_1	GPIN	GPIN
GPIN2	GPIN_2	GPIN	GPIN
GPIN3	GPIN_3	GPIN	GPIN
GPIN4	GPIN_4	GPIN	GPIN
GPIN5	GPIN_5	GPIN	GPIN
GPIN6	GPIN_6	GPIN	GPIN
GPIN7	GPIN_7	GPIN	GPIN
GPIN8	GPIN_8	GPIN	GPIN
GPIN9	GPIN_9	GPIN	GPIN
GPIN10	GPIN_10	GPIN	GPIN
GPIN11	GPIN_11	GPIN	GPIN
GPIN12	GPIN_12	GPIN	GPIN
GPIN13	GPIN_13	GPIN	GPIN
GPIN14	GPIN_14	GPIN	GPIN
GPIN15	GPIN_15	GPIN	GPIN
GPIN16	GPIN_16	GPIN	GPIN
GPIN17	GPIN_17	GPIN	GPIN
GPIN18	GPIN_18	GPIN	GPIN
GPIN19	GPIN_19	GPIN	GPIN
GPIN20	GPIN_20	GPIN	GPIN
GPIN21	GPIN_21	GPIN	GPIN
GPIN22	GPIN_22	GPIN	GPIN
GPIN23	GPIN_23	GPIN	GPIN
GPIN24	GPIN_24	GPIN	GPIN
GPIN25	GPIN_25	GPIN	GPIN
GPIN26	GPIN_26	GPIN	GPIN
GPIN27	GPIN_27	GPIN	GPIN
GPIN28	GPIN_28	GPIN	GPIN
GPIN29	GPIN_29	GPIN	GPIN
GPIN30	GPIN_30	GPIN	GPIN
GPIN31	GPIN_31	GPIN	GPIN
GPIN32	GPIN_32	GPIN	GPIN
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GPIN124	GPIN_124	GPIN	GPIN
GPIN125	GPIN_125	GPIN	GPIN
GPIN126	GPIN_126	GPIN	GPIN
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GPIN128	GPIN_128	GPIN	GPIN

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Schematic Page List

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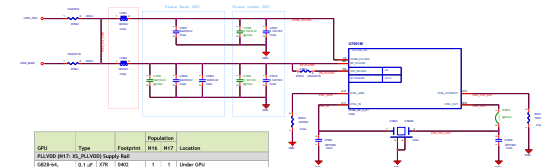
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9002)
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085_PW_***
086_PW_1.2V/+VTT/+2.5V(UP900
2)
087_PW_+3VADSW/+5VSUS(UP9
003)
088_PW_LOAD SWITCH
089.PW_CHARGER
090_PW_PROTECTION
091_PW_DGPU_2PHASE(uP902
4)
092_PW_
093_PW_1.35VSG
094_PW_
095_PW_***
096_PW_***
097_PW_
098_PW_***
099_PW_FLOW CHART
100_Power On Timing--AC mode



Network	Value	Footprint	Population	Location
VTT-DI	1.2V	100	1	Under GPU
VTT-DI	1.2V	100	1	Under GPU

STRAP	STRAP	STRAP
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000
L	1	0.000000

Resistor Values	Pull-Up to VTT_MAIN	Pull-Down to GND
4.99 kΩ	1000	0000
10.0 kΩ	1001	0001
15.0 kΩ	1010	0010
20.0 kΩ	1011	0011
24.9 kΩ	1100	0100
30.1 kΩ	1101	0101
34.9 kΩ	1110	0110
40.3 kΩ	1111	0111



Resistor Values	Pull-Up to VTT_MAIN	Pull-Down to GND
4.99 kΩ	1000	0000
10.0 kΩ	1001	0001
15.0 kΩ	1010	0010
20.0 kΩ	1011	0011
24.9 kΩ	1100	0100
30.1 kΩ	1101	0101
34.9 kΩ	1110	0110
40.3 kΩ	1111	0111

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Schematic Page List

Hide

- 083_PW_+1.0VSUS/1.8VSUS(UP9002)
- 084_PW_
- 085_PW_***
- 086_PW_1.2V/+VTT/+2.5V(UP9002)
- 087_PW_+3VADSW/+5VSUS(UP9003)
- 088_PW_LOAD SWITCH
- 089.PW_CHARGER
- 090_PW_PROTECTION
- 091_PW_DGPU_2PHASE(uP9004)
- 092_PW_
- 093_PW_1.35VSG
- 094_PW_
- 095_PW_***
- 096_PW_***
- 097_PW_
- 098_PW_***
- 099_PW_FLOW CHART
- 100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

088

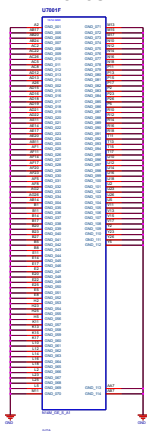
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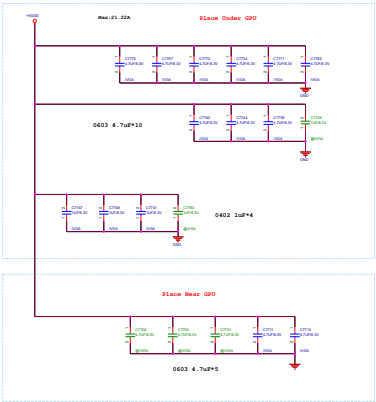
Next Page

MoveTo

NVDD GROUND



NVDD POWER AND DECOUPLING



330uF & 22uF reserved in NVDD Power page

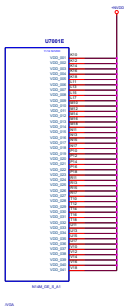


Table 3. NVDD and NVDD5 Decoupling and Filtering

GPU	Capacitor Type	Footprint	Population N16 (N17)	Location
NVDD Supply Net				
GR2B-64, GR2C-64	4.7 uF X65	0603	10	Under GPU
	1 uF X65	0402	4	Under GPU
	47 uF X58	0805	1	Near GPU
	10 uF X78	0805	-	Near GPU
	22 uF X58	0805	1	Near GPU
	4.7 uF X58	0805	4	Near GPU
	330 uF POS	7343	1	Near GPU
NVDD5 Supply Net				
GR2C-64 Only	4.7 uF X65	0603	N/A	Under GPU
	1 uF X65	0402	N/A	Under GPU
	10 uF X65	0805	N/A	Near GPU
	22 uF X65	0805LP	N/A	Near GPU
	330 uF POS	7343	N/A	Near GPU

In the case of a merged rail configuration with a single VDD core power rail, add the NVDD5 decoupling caps onto the NVDD supply.

Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+3VADSW/+5VSUS(UP9003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP9004)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

088

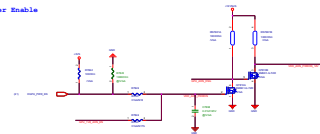
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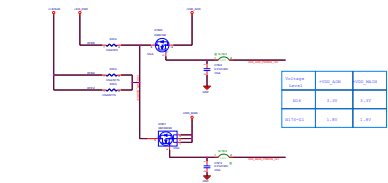
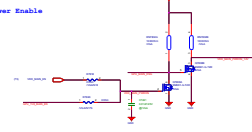
Next Page

MoveTo

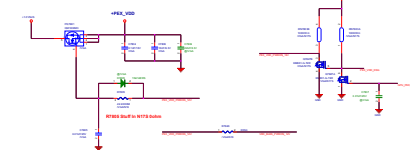
VDD_A0H Power Enable



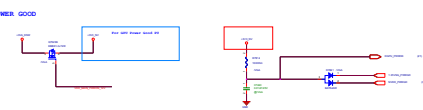
VDD_MAIN Power Enable



PEX_VDD Power Enable



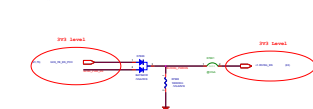
GPU POWER GOOD



WVDD Power Enable

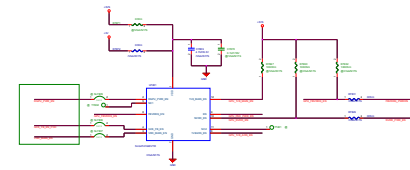


FSWVDDQ Power Enable



M175 GPU Sequence Solution

Input Signal for M175



Output Signal for M175(3.3V)

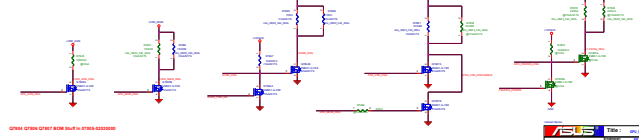
1. GPU_POWER_GOOD

2. GPU_POWER_GOOD_N

3. GPU_POWER_GOOD_N

4. GPU_POWER_GOOD_N

GPU Power Discharge



Schematic Page List

Hide

U05_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW_CHARGER

090 PW PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

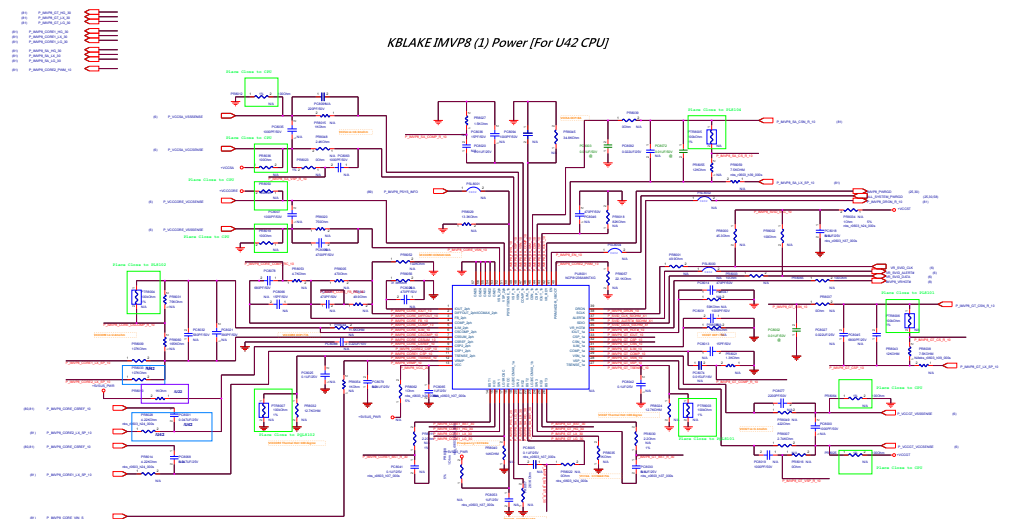
088

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+3VADSW/+5VSUS(UP9003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP9004)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

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100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

088

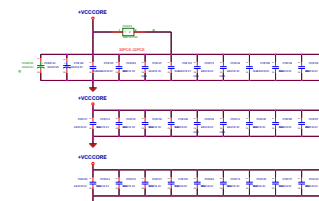
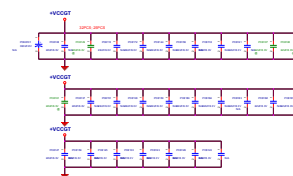
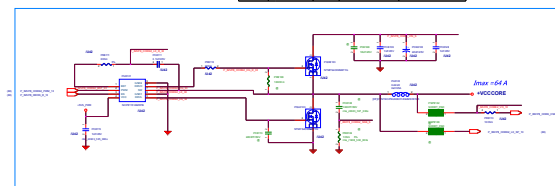
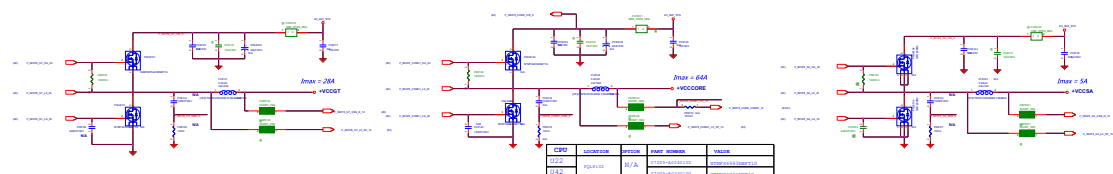
Toggle FullScreen

Pre Page

Next Page

MoveTo

Kaby Lake-U IMVP8 Power (2) [For CPU]



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+3VADSW/+5VSUS(UP9003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP9004)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

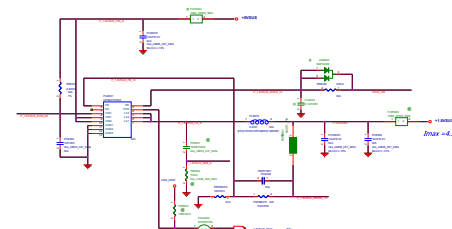
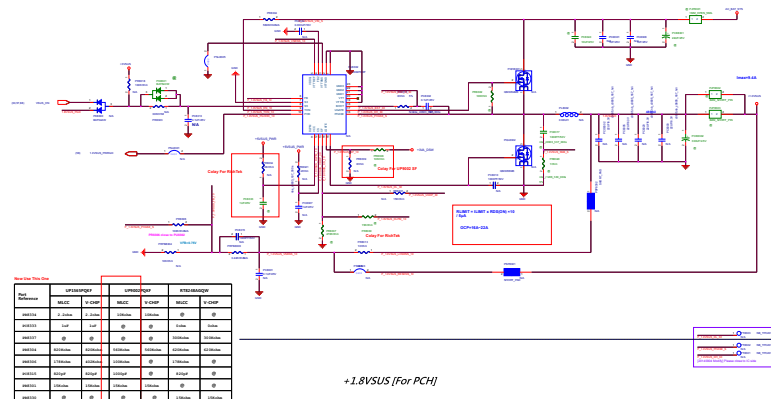
088

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP9002)

087_PW_+3VADSW/+5VSUS(UP9003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP9004)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

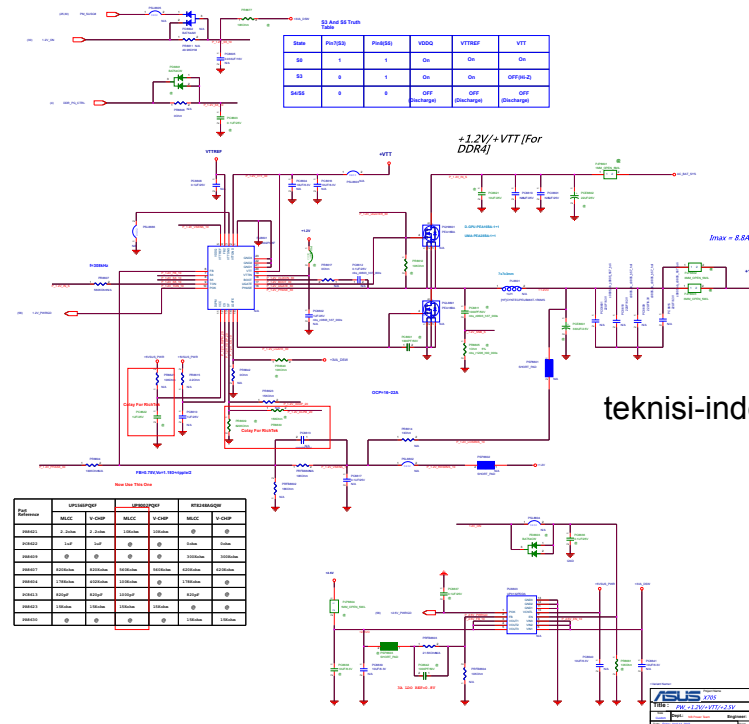
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Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

U05_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085 PW ***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW CHARGER

090 PW PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092 PW

093 PW 1.35VSG

094_PW_

095 PW ***

096 PW ***

097 PW

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

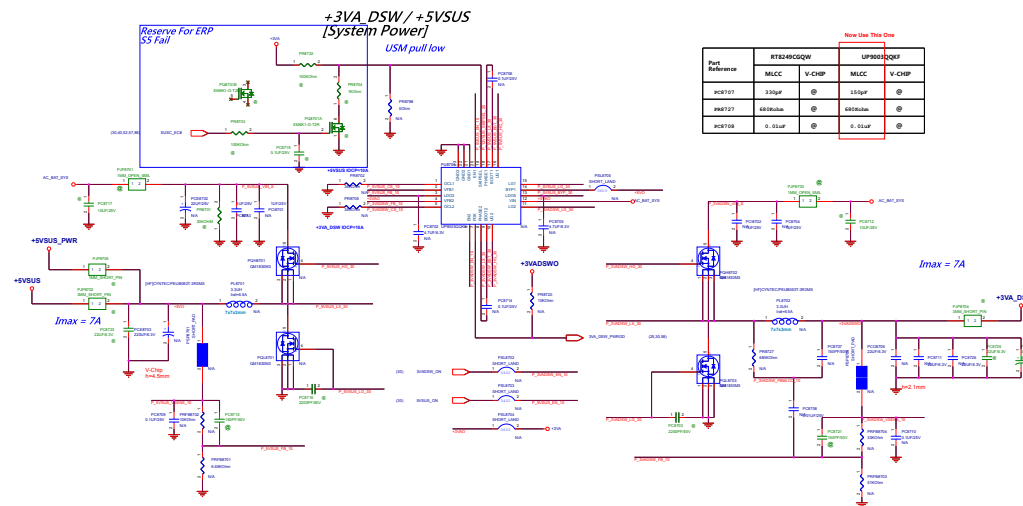
088

Toggle FullScreen

Pre Page

Next Page

MoveTo



Part Reference	RTE245CGQW		UP3022QKQ	
	MLCC	V-CHIP	MLCC	V-CHIP
PCB707	330pF	⊗	150pF	⊗
PCB727	680nF	⊗	680nF	⊗
PCB708	0.01uF	⊗	0.01uF	⊗

[illegible]

Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

076

077

078

080

081

083

086

087

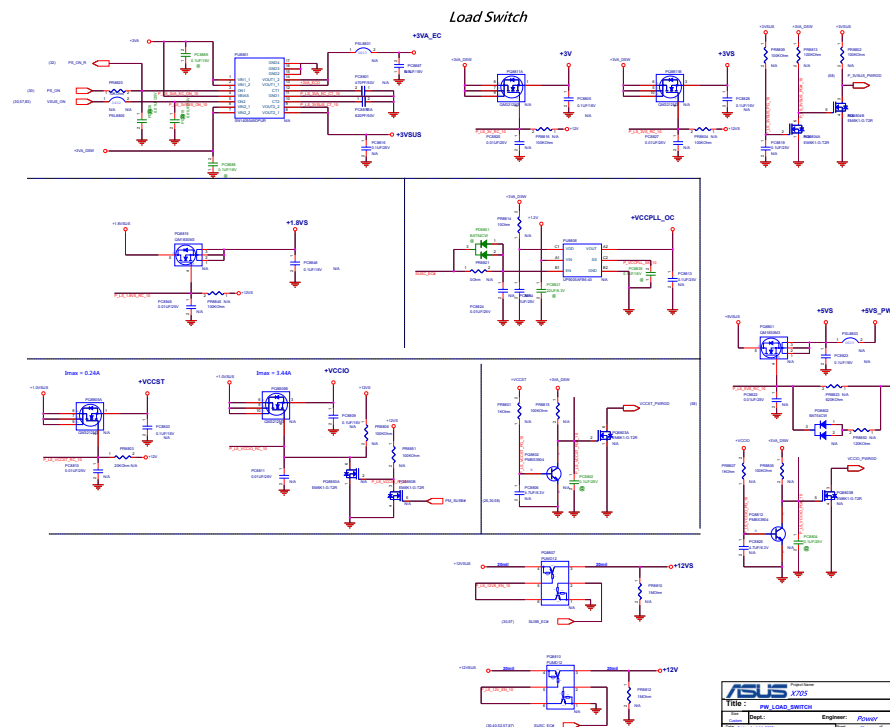
088

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

U83_PW_+1.0VSUS/1.8VSUS(OP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW_CHARGER

090 PW PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095 PW ***

096_PW_***

097 PW

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

089

090

091

093

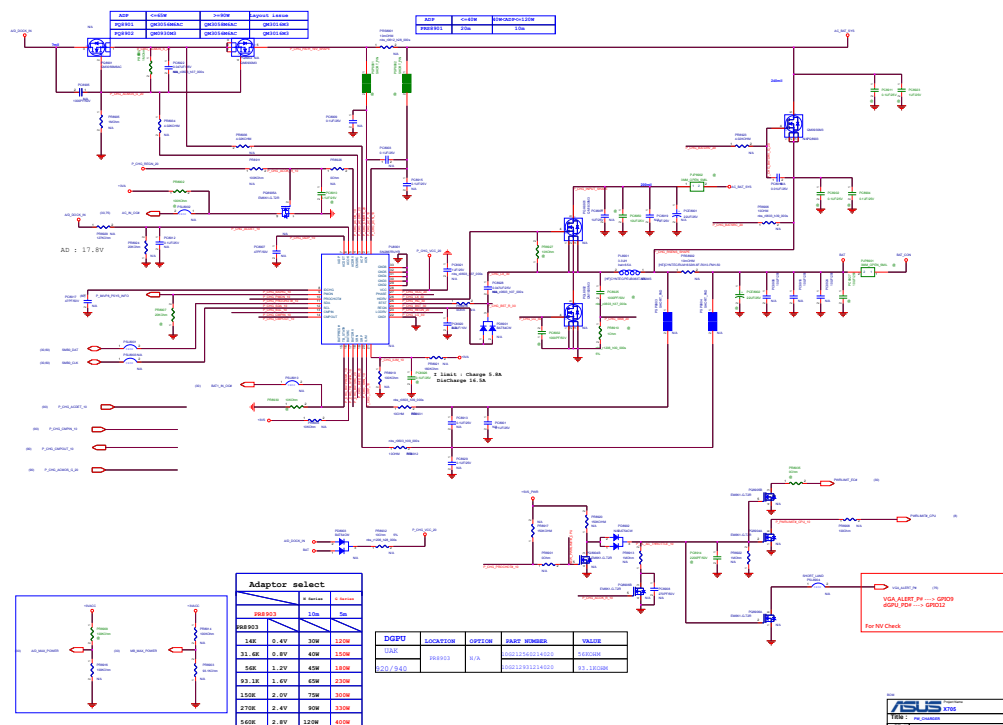
099

Toggle FullScreen

Pre Page

Next Page

MoveTo



Adaptor select				
		4.0mm	4.5mm	
		10m	5m	
98B903				
14K	0.4V	30W	120W	
31.6K	0.8V	40W	150W	
56K	1.2V	45W	180W	
93.1K	1.6V	65W	230W	
150K	2.0V	75W	300W	
270K	2.4V	90W	330W	
430K	2.8V	120W	400W	

DGPU	LOCATION	OPTION	PART NUMBER	VALUE
UAK	FR8803	N/A	100212540214020	56XONM
670 (5.6)			100212831214020	83.18XONM

		Project Name: XT05	
Title: FW_CHARGER			
Dept.: Ad Power Team	Engineer: Power		

Schematic Page List

Hide

U05_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)

087_PW_+3VADSW/+5VSUS(UP9
003)

088 PW LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

089

090

091

093

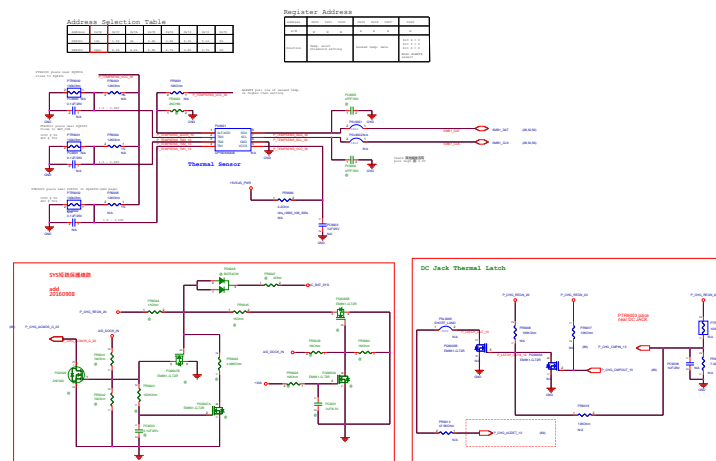
099

Toggle FullScreen

Pre Page

Next Page

MoveTo



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Schematic Page List

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083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

089

090

091

093

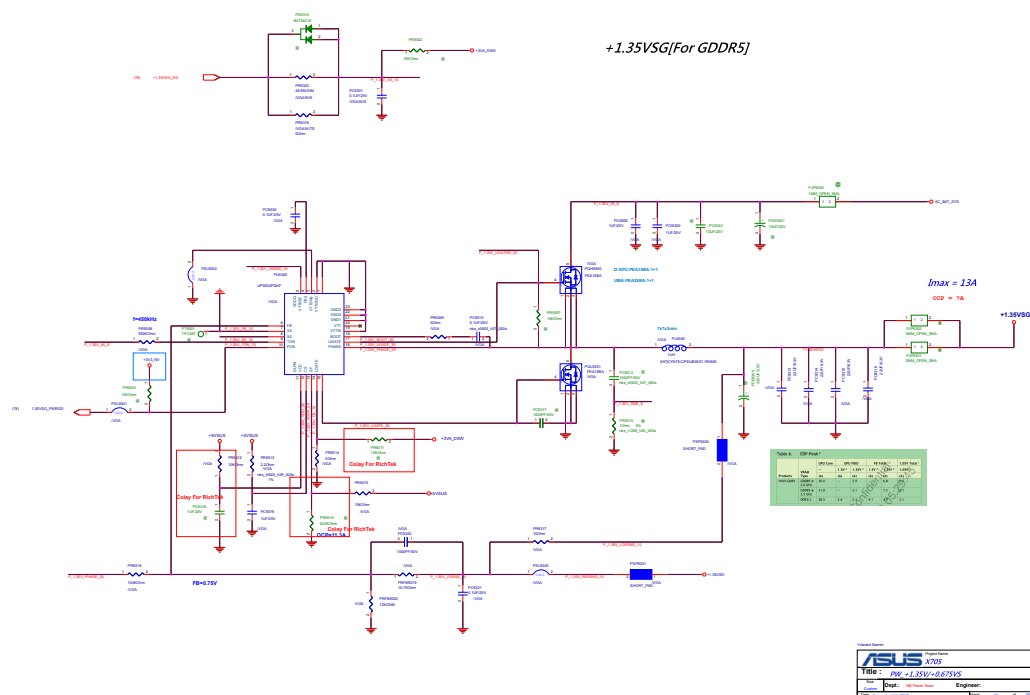
099

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

- 083_PW_+1.0VSUS/1.8VSUS(UP9002)
- 084_PW_
- 085_PW_***
- 086_PW_1.2V/+VTT/+2.5V(UP9002)
- 087_PW_+3VADSW/+5VSUS(UP9003)
- 088_PW_LOAD SWITCH
- 089.PW_CHARGER
- 090_PW_PROTECTION
- 091_PW_DGPU_2PHASE(uP9004)
- 092_PW_
- 093_PW_1.35VSG
- 094_PW_
- 095_PW_***
- 096_PW_***
- 097_PW_
- 098_PW_***
- 099_PW_FLOW CHART
- 100_Power On Timing--AC mode

Global Search

089

090

091

093

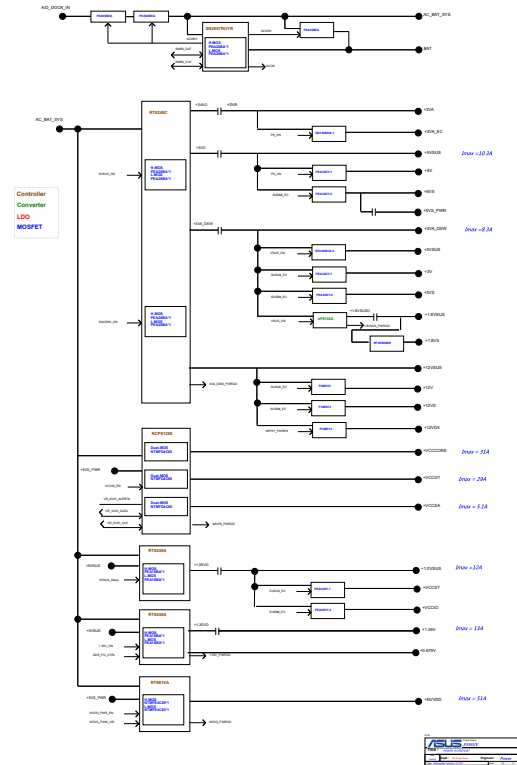
099

Toggle FullScreen

Pre Page

Next Page

MoveTo



Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

100

101

102

Toggle FullScreen

Pre Page

Next Page

MoveTo

AC-IN Mode

1 +3VA/+5VA/+3VA_EC
(to EC) 2 EC_RST#
(EC to power) 3 VSUS_ON
+3VSUS/+5VSUS
(PCR to EC) 4 ME_SusPwrOnAck
(power to EC) 5 SUS_PWRGD
(EC to PCR) 6 PM_RSMRST#
(EC to PCR) 7 AC_PRESENT
(to EC) 8 PM_SM#
(EC to PCR) 9 PM_RSMRST#
(PCR to EC) 10 PM_RSLP_A#
(PCR to EC) 11 PM_RSLP#
12 PM_RSMRST#/SLP_LAN#
(PCR to EC) (PCR to power)
+1.1VM_LAN#
(EC to power) ME_RSLP_R#
+1.1VM/+3VM
(EC to power) 13 SUSB_R#
+0.8V/+1.2V/+1.8V/+3V/+5V
(EC to power) 14 SUSB_R#
+1.05V/+1.2V/+1.8V/+3V/+5V
(power to EC) ME_VRM_PWRGD
(EC to PCR) ME_PWRCK
15 SYSTEM_PWRGD
+VTT_CPU
(CPU to power) GPF_VR_ON
16 +VTT_CPU_PWRGD/ 17 H_VTT_PWRGD
(power to CPU) GPF_VID
+VGGP_CORE
(power to EC) GPF_PWRGD
18 All_SYSTEM_PWRGD
(EC to power) CPU_VRM#
19 +VOCIN
CIS_PWRGD
(conversion of CIS_R#)
(power to EC) 20 CORE_PWRGD
(EC to PCR) 21 PM_PWRCK
(PCR to CPU) H_DRAM_PWRGD
(PCR to CPU) H_CPU_PWRGD
(PCR to CPU) 22 BUF_PLT_RST#

UX303JA Power-On Sequence
Timing Diagram Rev.0.1

Schematic Page List

Hide

083_PW_+1.0VSUS/1.8VSUS(UP
9002)

084_PW_

085_PW_***

086_PW_1.2V/+VTT/+2.5V(UP900
2)087_PW_+3VADSW/+5VSUS(UP9
003)

088_PW_LOAD SWITCH

089.PW_CHARGER

090_PW_PROTECTION

091_PW_DGPU_2PHASE(uP902
4)

092_PW_

093_PW_1.35VSG

094_PW_

095_PW_***

096_PW_***

097_PW_

098_PW_***

099_PW_FLOW CHART

100_Power On Timing--AC mode

Global Search

100

101

102

Toggle FullScreen

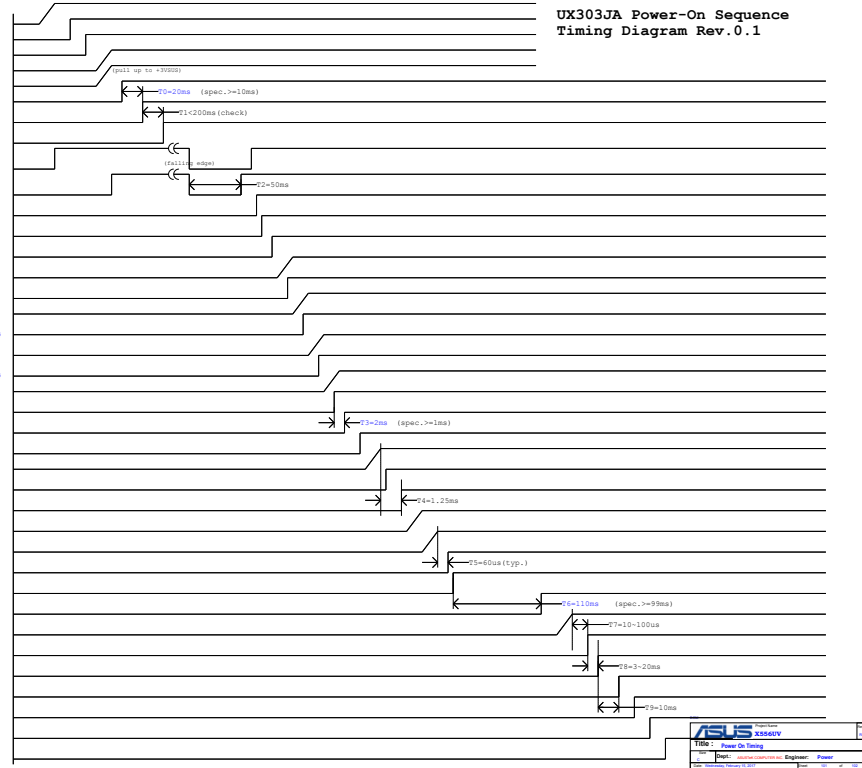
Pre Page

Next Page

MoveTo

AC-IN Mode

1 +3VA/+5VA/+3VA_EC
(to EC) 2 EC_RST#
(EC to power) 3 VSUS_ON
+3VSUS/+5VSUS
(PCR to EC) 4 ME_SusPwrInAck
(power to EC) 5 SUS_PWRGD
(EC to PCR) 6 PM_RSMRST#
(EC to PCR) 7 AC_PRESENT#
(to EC) 8 PW_SW#
(EC to PCR) 9 PW_PRRST#
(PCR to EC) 10 PW_STP_A#
(PCR to EC) 11 PM_RST#
12 PM_RST#/SLP_LAN#
(PCR to EC) (PCR to power)
+1.1VM_LAN
ME_STP_M_RST#
+1.1VM/+3VM
(EC to power) 13 RSMC_RST#
+0.6V/+1.2V/+1.8V/+3V/+5V
(EC to power) 14 SUSB_RST#
+1.05V/+1.2V/+1.8V/+3V/+5V
(power to EC) ME_VPM_PWRGD
(EC to PCR) ME_PWRDR
15 SYSTEM_PWRGD
+VTT_CPU
(CPU to power) GFX_VB_ON
16 +VTT_CPU_PWRGD/ 17 B_VTTPWRGD
(power to CPU) GFX_VID
+VDPFX_CORE
(power to EC) GFX_PWRGD
18 ALL_SYSTEM_PWRGD
(EC to power) CPU_VBON
19 +VCCIN
CLKA_PWRGD
(inversion of CLK_A#)
(power to EC) 20 CORE_PWRGD
(EC to PCR) 21 PM_PWRDR
(PCR to CPU) B_DRN#_PWRGD
(PCR to CPU) B_CUPWRGD
(PCR to CPU) 22 BPT_RST_RST#

UX303JA Power-On Sequence
Timing Diagram Rev.0.1

Schematic Page List

Hide

- 083_PW_+1.0VSUS/1.8VSUS(UP9002)
- 084_PW_
- 085_PW_***
- 086_PW_1.2V/+VTT/+2.5V(UP9002)
- 087_PW_+3VADSW/+5VSUS(UP9003)
- 088_PW_LOAD SWITCH
- 089.PW_CHARGER
- 090_PW_PROTECTION
- 091_PW_DGPU_2PHASE(uP9004)
- 092_PW_
- 093_PW_1.35VSG
- 094_PW_
- 095_PW_***
- 096_PW_***
- 097_PW_
- 098_PW_***
- 099_PW_FLOW CHART
- 100_Power On Timing--AC mode

Global Search

100

101

102

Toggle FullScreen

Pre Page

Next Page

MoveTo

